IFEC'16

IEEE IFEC 2016 Guideline for Final Report

The objective of the final report is to reveal the performance of your hardware prototype circuit. The final report should be prepared in PDF format with A4 size, single column, single space, no smaller than 12 point font, maximum length 20 pages and should be submitted to the IFEC2016 website (http://cc.ee.ntu.edu.tw/~eepro/IFEC2016/index.html) before the deadline (6th July 2016). Please follow the following guide lines to prepare your final report. Your final report should include but NOT LIMITED to the following items.

1. A cover page shows the team's information, including: report title, affiliation, address, country, team members' information (position, name, e-mail, and expected graduation date). The cover page is exclusive of the 20 page limitation.
2. Explanation of the circuit topology, such as: operation modes and important features.
3. Explanation of the controller, such as: control method and feedback loop design. (A control flow chart of the program is required if your controller is realized by a Micro Control Unit.)
4. Using computer simulations to demonstrate the expected performance (output voltage or input current) of the converter.
5. Hardware implementation including: component design and selection, PCB layout, specifications of key components, volume calculation.
6. Demonstrate your experimental measurements effectively using tables, waveforms, charts, or pictures.
7. Explain how each team member shares the work load.
8. Your converter's board of materials (BOM). The total cost of the BOM using the price listed in the Digi-Key (http://www.digikey.com/) or the RS components (http://uk.rs-online.com/web/) should be included. Put the BOM in the Appendix, which is exclusive of the 20 page limitation.
9. Other materials from the hardware experimental that can enhanced your team's achievement.