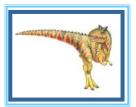
# **Chapter 6: Synchronization**



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#### **Module 6: Synchronization**

- Background
- The Critical-Section Problem
- Peterson's Solution
- Synchronization Hardware
- Semaphores
- Classic Problems of Synchronization
- Monitors
- Synchronization Examples
- Atomic Transactions



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#### **Objectives**

- To introduce the critical-section problem, whose solutions can be used to ensure the consistency of shared data
- To present both software and hardware solutions of the critical-section problem
- To introduce the concept of an atomic transaction and describe mechanisms to ensure atomicity



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#### **Background**

- Concurrent access to shared data may result in data inconsistency
- Maintaining data consistency requires mechanisms to ensure the orderly execution of cooperating processes

Suppose that we wanted to provide a solution to the consumer-producer problem that fills all the buffers.

- We can do so by having an integer count that keeps track of the number of full buffers.
- Initially, count is set to 0.
- It is incremented by the producer after it produces a new buffer and is decremented by the consumer after it consumes a buffer.

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```
Producer

while (true) {

/* produce an item and put in nextProduced */
while (count == BUFFER_SIZE)
; // do nothing

buffer [in] = nextProduced;
in = (in + 1) % BUFFER_SIZE;
count++;
}
```

```
Consumer

while (true) {
    while (count == 0)
        ; // do nothing

nextConsumed = buffer[out];
    out = (out + 1) % BUFFER_SIZE;
    count--;

// consume the item in nextConsumed
}

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```



#### **Race Condition**

■ count++ could be implemented as

```
register1 = count
register1 = register1 + 1
count = register1
```

count-- could be implemented as

```
register2 = count
register2 = register2 - 1
count = register2
```

■ Consider this execution interleaving with "count = 5" initially:

```
S0: producer execute register1 = count {register1 = 5}
S1: producer execute register1 = register1 + 1 {register1 = 6}
S2: consumer execute register2 = count {register2 = 5}
S3: consumer execute register2 = register2 - 1 {register2 = 4}
S4: consumer execute count = register2 {count = 4}
S5: producer execute count = register1 {count = 6}
```

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# A General Fremework for Synchronization the Critical-Section Problem

permission request entry section;

critical section;

exit notification 
exit section;

remainder section; } while (1);

#### Assumptons:

- Atomic execution of each statement line
- Interleaving execution among processes



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- 1. Mutual Exclusion If process P<sub>i</sub> is executing in its critical section, then no other processes can be executing in their critical sections
- Progress If no process is executing in its critical section and there
  exist some processes that wish to enter their critical section, then
  the selection of the processes that will enter the critical section
  next cannot be postponed indefinitely
- Bounded Waiting A bound must exist on the number of times that other processes are allowed to enter their critical sections after a process has made a request to enter its critical section and before that request is granted
  - Assume that each process executes at a nonzero speed
  - No assumption concerning relative speed of the N processes



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#### **Peterson's Solution**

- Two process solution
- Assume that the LOAD and STORE instructions are atomic; that is, cannot be interrupted.
- The two processes share two variables:
  - int turn;
  - Boolean flag[2]
- The variable turn indicates whose turn it is to enter the critical section.
- The flag array is used to indicate if a process is ready to enter the critical section. flag[i] = true implies that process P<sub>i</sub> is ready!



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#### Algorithm for Process Pi

do {

- flag[i] = TRUE;1.
- 2. turn = 1-i;
- 3. while (flag[1-i] && turn == 1-i);
- 4. critical section
- flag[i] = FALSE; 5.
- remainder section 6.

} while (TRUE);





#### **Peterson's Solution Proof for the mutual exclusion (1/2)**

**Lemma:** When a Pi is is in the entry and the critical sections, flag[i] = true.

For convenience, a state is denoted as [h,k,x,y,z] where h, k,x,y,z are respectively the statement index of P0, statement index of P1, value of turn, value of flag[0], and value of flag[1].

According to the lemma, we assume that [4,4,0,1,1] happens. This implies that P0 enters the critical section last from [3,4,0,1,1].





There are two possibilities of the predecessor to [3,4,0,1,1].

- One possible predecessor of [3,4,0,1,1] is [3,3,0,1,1] which is impossible.
  - From [3,3,0,1,1], the while loop condition for P1 is false.
- The other possible predecessor of [3,4,0,1,1] is [2,3,?,1,1] which is also impossible.
  - From [2,3,?,1,1], statement 2 for P0 changes turn to 1 instead of 0.

Since both possibilities are contradictions, the assumption of violation of mutual exclusion is a contradiction.

Thus the lemma is proven. ■

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# Peterson's Solution Properties

- Mutual Exclusion
  - The eventual value of *turn* determines which process enters the critical section.
- Progress
  - A process can only be stuck in the while loop, and the process which can keep it waiting must be in its critical sections.
- Bounded Waiting
  - Each process wait at most one entry by the other process.



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#### The critical-section problem A solution for n processes

#### **Bakery Algorithm**

- Originally designed for distributed systems
- Token-based
  - Processes which are ready to enter their critical section must take a number and wait till the number becomes the lowest.
- Two arrays of local variables
  - int number[i]:
    - ▶ Pi's token number if it is nonzero.
  - boolean choosing[i]:
    - Pi is taking a number.





#### The critical-section problem A solution for n processes

do {

```
choosing[i]=true;
number[i]=max(number[0], ...number[n-1])+1;
choosing[i]=false;
for (j=0; j < n; j++) {
  while choosing[j];
  while (number[j] != 0 && (number[j],j)<(number[i],i));</pre>
```

critical section

number[i]=0;

remainder section

} while (1);

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An observation: If

- Pi is in its critical section, and
- Pk (k != i) has already chosen its number[k],

then (number[i],i) < (number[k],k)



#### **Synchronization Hardware**

- Many systems provide hardware support for critical section code
- Uniprocessors could disable interrupts
  - Currently running code would execute without preemption
  - Generally too inefficient on multiprocessor systems
    - Operating systems using this not broadly scalable
- Modern machines provide special atomic hardware instructions
  - ▶ Atomic = non-interruptable
  - Either test memory word and set value
  - Or swap contents of two memory words



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# 

```
TestAndndSet Instruction

Definition:

boolean TestAndSet (boolean *target)
{
 boolean rv = *target;
 *target = TRUE;
 return rv:
}

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```

```
Solution using TestAndSet

Shared boolean variable lock., initialized to false.
Solution:

do {
    while (TestAndSet (&lock ))
    ; // do nothing

    // critical section

lock = FALSE;

// remainder section
} while (TRUE);
```

```
Swap Instruction

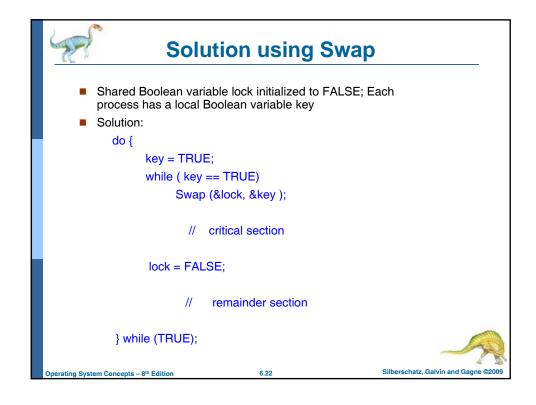
• Definition:

void Swap (boolean *a, boolean *b)
{
    boolean temp = *a;
    *a = *b;
    *b = temp:
}

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```



```
Bounded-waiting Mutual Exclusion with TestandSet()
       do {
             waiting[i] = TRUE;
             key = TRUE;
             while (waiting[i] && key)
                        key = TestAndSet(&lock);
             waiting[i] = FALSE;
                        // critical section
             j = (i + 1) \% n;
             while ((j != i) && !waiting[j])
                        j = (j + 1) \% n;
             if (j == i)
                        lock = FALSE;
             else
                        waiting[j] = FALSE;
                        // remainder section
       } while (TRUE);
```

```
Semaphore

Synchronization tool that does not require busy waiting
Semaphore S – integer variable
Two standard operations modify S: wait() and signal()
Originally called P() and V()
Less complicated
Can only be accessed via two indivisible (atomic) operations

wait (S) {
while S <= 0
; // no-op
S--;
}
signal (S) {
S++;
}
```



- Counting semaphore integer value can range over an unrestricted domain
- Binary semaphore integer value can range only between 0 and 1; can be simpler to implement
  - Also known as mutex locks
- Can implement a counting semaphore S as a binary semaphore
- Provides mutual exclusion

```
Semaphore mutex; // initialized to 1
do {
    wait (mutex);
    // Critical Section
    signal (mutex);
    // remainder section
} while (TRUE);
```



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#### **Semaphore Implementation**

- Must guarantee that no two processes can execute wait () and signal () on the same semaphore at the same time
- Thus, implementation becomes the critical section problem where the wait and signal code are placed in the critical section.
  - Could now have busy waiting in critical section implementation
    - But implementation code is short
    - Little busy waiting if critical section rarely occupied
- Note that applications may spend lots of time in critical sections and therefore this is not a good solution.



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#### Semaphore Implementation with no Busy waiting

- With each semaphore there is an associated waiting queue. Each entry in a waiting queue has two data items:
  - value (of type integer)
  - pointer to next record in the list
- Two operations:
  - block place the process invoking the operation on the appropriate waiting queue.
  - wakeup remove one of processes in the waiting queue and place it in the ready queue.



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#### Semaphore Implementation with no Busy waiting (Cont.)

```
Implementation of wait:
wait(semaphore *S) {
```

```
wait(semaphore *S) {
    S->value--;
    if (S->value < 0) {
        add this process to S->list;
        block();
    }
```

Implementation of signal:

```
signal(semaphore *S) {
     S->value++;
     if (S->value <= 0) {
          remove a process P from S->list;
          wakeup(P);
     }
}
```

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#### **Deadlock and Starvation**

- Deadlock two or more processes are waiting indefinitely for an event that can be caused by only one of the waiting processes
- Let S and Q be two semaphores initialized to 1

- Starvation indefinite blocking. A process may never be removed from the semaphore queue in which it is suspended
- Priority Inversion Scheduling problem when lower-priority process holds a lock needed by higher-priority process



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#### **Classical Problems of Synchronization**

- Bounded-Buffer Problem
- Readers and Writers Problem
- Dining-Philosophers Problem



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#### **Bounded-Buffer Problem**

- N buffers, each can hold one item
- Semaphore mutex initialized to the value 1
- Semaphore full initialized to the value 0
- Semaphore empty initialized to the value N.



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```
Bounded Buffer Problem (Cont.)

The structure of the producer process
```

```
do {
      // produce an item in nextp
      wait (empty);
      wait (mutex);

      // add the item to the buffer
      signal (mutex);
      signal (full);
} while (TRUE);
```

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# **Bounded Buffer Problem (Cont.)**

■ The structure of the consumer process

```
do {
    wait (full);
    wait (mutex);

    // remove an item from buffer to nextc

    signal (mutex);
    signal (empty);

    // consume the item in nextc

} while (TRUE);
```

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#### **Readers-Writers Problem**

- A data set is shared among a number of concurrent processes
  - Readers only read the data set; they do not perform any updates
  - Writers can both read and write
- Problem allow multiple readers to read at the same time. Only one single writer can access the shared data at the same time
- Shared Data
  - Data set
  - Semaphore mutex initialized to 1
  - Semaphore wrt initialized to 1
  - Integer readcount initialized to 0



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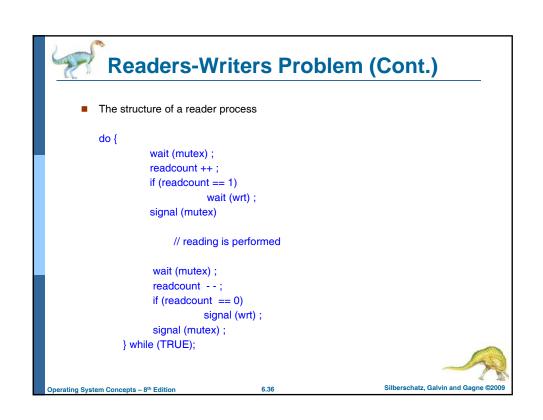
```
Readers-Writers Problem (Cont.)

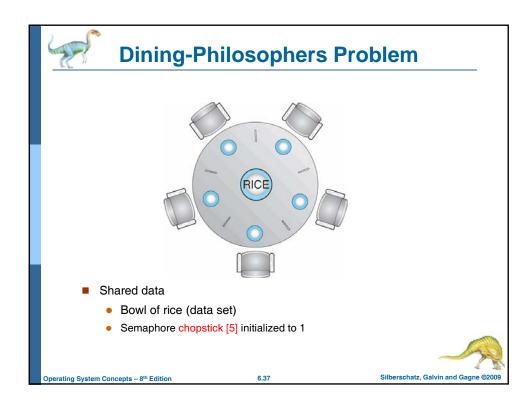
The structure of a writer process

do {
    wait (wrt);
    // writing is performed
    signal (wrt);
} while (TRUE);

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```





# ■ The structure of Philosopher i: do { wait ( chopstick[i] ); wait ( chopStick[ (i + 1) % 5] ); // eat signal ( chopstick[i] ); signal (chopstick[ (i + 1) % 5] ); // think } while (TRUE);



# **Problems with Semaphores**

- Correct use of semaphore operations:
  - signal (mutex) .... wait (mutex)
  - wait (mutex) ... wait (mutex)
  - Omitting of wait (mutex) or signal (mutex) (or both)



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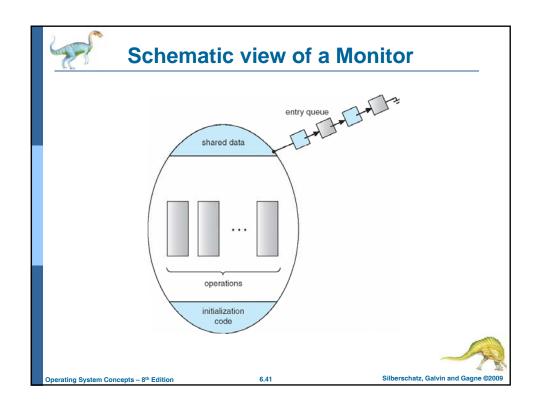


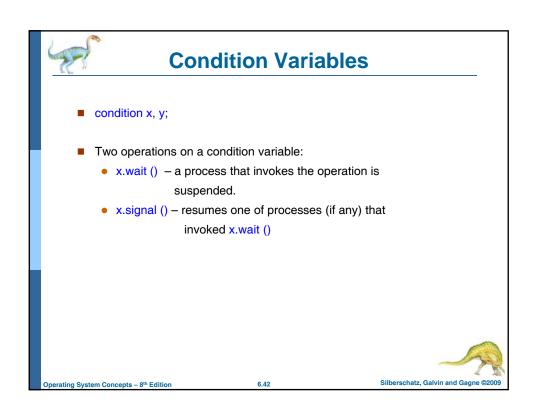
#### **Monitors**

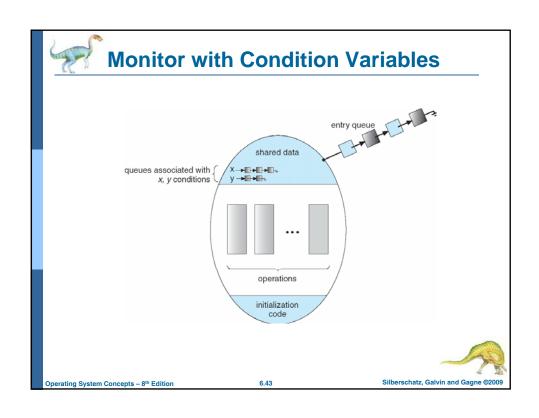
- A high-level abstraction that provides a convenient and effective mechanism for process synchronization
- Only one process may be active within the monitor at a time

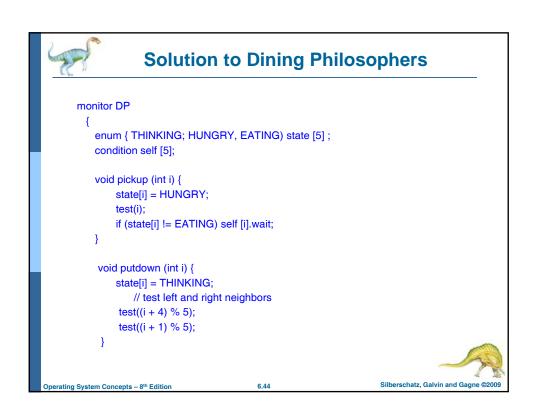
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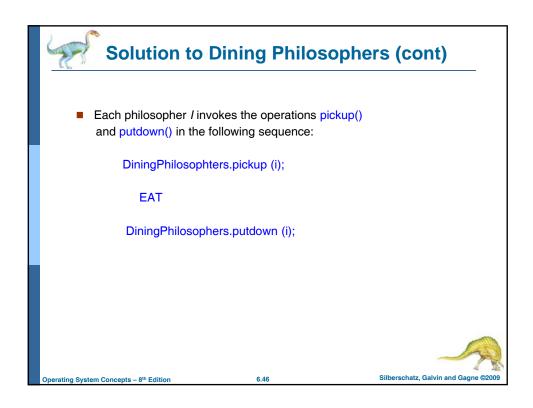
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```
Monitor Implementation Using Semaphores

Semaphore mutex; // (initially = 1)
Semaphore next; // (initially = 0)
int next-count = 0;

Each procedure F will be replaced by

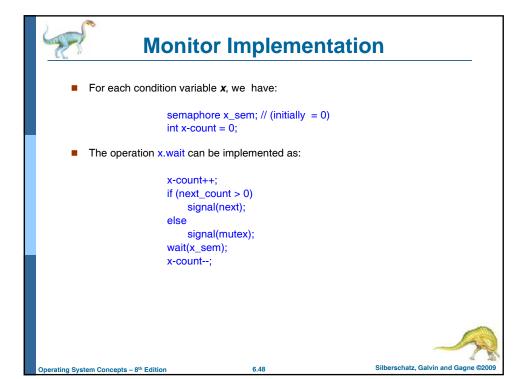
wait(mutex);

body of F;

...

if (next_count > 0)
signal(next)
else
signal(mutex);

Mutual exclusion within a monitor is ensured.
```



```
Monitor Implementation

The operation x.signal can be implemented as:

if (x-count > 0) {
    next_count++;
    signal(x_sem);
    wait(next);
    next_count--;
}

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```

```
monitor ResourceAllocator
{
boolean busy;
condition x;
void acquire(int time) {
    if (busy)
        x.wait(time);
    busy = TRUE;
}
void release() {
    busy = FALSE;
    x.signal();
}
initialization code() {
    busy = FALSE;
}
}
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```



#### **Synchronization Examples**

- Solaris
- Windows XP
- Linux
- Pthreads



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#### **Solaris Synchronization**

- Implements a variety of locks to support multitasking, multithreading (including real-time threads), and multiprocessing
- Uses adaptive mutexes for efficiency when protecting data from short code segments
- Uses condition variables and readers-writers locks when longer sections of code need access to data
- Uses turnstiles to order the list of threads waiting to acquire either an adaptive mutex or reader-writer lock



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# **Windows XP Synchronization**

- Uses interrupt masks to protect access to global resources on uniprocessor systems
- Uses spinlocks on multiprocessor systems
- Also provides dispatcher objects which may act as either mutexes and semaphores
- Dispatcher objects may also provide events
  - An event acts much like a condition variable



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#### **Linux Synchronization**

- Linux:
  - Prior to kernel Version 2.6, disables interrupts to implement short critical sections
  - Version 2.6 and later, fully preemptive
- Linux provides:
  - semaphores
  - spin locks



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# **Pthreads Synchronization**

- Pthreads API is OS-independent
- It provides:
  - mutex locks
  - condition variables
- Non-portable extensions include:
  - read-write locks
  - spin locks



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# **Atomic Transactions**

- System Model
- Log-based Recovery
- Checkpoints
- Concurrent Atomic Transactions



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#### **System Model**

- Assures that operations happen as a single logical unit of work, in its entirety, or not at all
- Related to field of database systems
- Challenge is assuring atomicity despite computer system failures
- Transaction collection of instructions or operations that performs single logical function
  - Here we are concerned with changes to stable storage disk
  - Transaction is series of read and write operations
  - Terminated by commit (transaction successful) or abort (transaction failed) operation
  - Aborted transaction must be rolled back to undo any changes it performed



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#### **Types of Storage Media**

- Volatile storage information stored here does not survive system crashes
  - Example: main memory, cache
- Nonvolatile storage Information usually survives crashes
  - Example: disk and tape
- Stable storage Information never lost
  - Not actually possible, so approximated via replication or RAID to devices with independent failure modes

Goal is to assure transaction atomicity where failures cause loss of information on volatile storage



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#### **Log-Based Recovery**

- Record to stable storage information about all modifications by a transaction
- Most common is write-ahead logging
  - Log on stable storage, each log record describes single transaction write operation, including
    - Transaction name
    - Data item name
    - Old value
    - New value
  - <T<sub>i</sub> starts> written to log when transaction T<sub>i</sub> starts
  - <T<sub>i</sub> commits> written when T<sub>i</sub> commits
- Log entry must reach stable storage before operation on data occurs



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# **Log-Based Recovery Algorithm**

- Using the log, system can handle any volatile memory errors
  - Undo(T<sub>i</sub>) restores value of all data updated by T<sub>i</sub>
  - Redo(T<sub>i</sub>) sets values of all data in transaction T<sub>i</sub> to new values
- Undo(T<sub>i</sub>) and redo(T<sub>i</sub>) must be idempotent
  - Multiple executions must have the same result as one execution
- If system fails, restore state of all updated data via log
  - If log contains <T<sub>i</sub> starts> without <T<sub>i</sub> commits>, undo(T<sub>i</sub>)
  - If log contains <T<sub>i</sub> starts> and <T<sub>i</sub> commits>, redo(T<sub>i</sub>)



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#### **Checkpoints**

- Log could become long, and recovery could take long
- Checkpoints shorten log and recovery time.
- Checkpoint scheme:
  - 1. Output all log records currently in volatile storage to stable storage
  - 2. Output all modified data from volatile to stable storage
  - 3. Output a log record <checkpoint> to the log on stable storage
- Now recovery only includes Ti, such that Ti started executing before the most recent checkpoint, and all transactions after Ti All other transactions already on stable storage



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#### **Concurrent Transactions**

- Must be equivalent to serial execution serializability
- Could perform all transactions in critical section
  - Inefficient, too restrictive
- Concurrency-control algorithms provide serializability



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# Serializability

- Consider two data items A and B
- Consider Transactions T<sub>0</sub> and T<sub>1</sub>
- Execute T<sub>0</sub>, T<sub>1</sub> atomically
- Execution sequence called schedule
- Atomically executed transaction order called serial schedule
- For N transactions, there are N! valid serial schedules



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# Schedule 1: T<sub>0</sub> then T<sub>1</sub>

$T_0$	$T_1$
read(A)	
write(A)	
read(B)	
write(B)	
	read(A)
	write(A)
	read(B)
	write(B)
'	

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#### **Nonserial Schedule**

- Nonserial schedule allows overlapped execute
  - Resulting execution not necessarily incorrect
- Consider schedule S, operations O<sub>i</sub>, O<sub>i</sub>
  - Conflict if access same data item, with at least one write
- If O<sub>i</sub>, O<sub>j</sub> consecutive and operations of different transactions & O<sub>i</sub> and O<sub>j</sub> don't conflict
  - Then S' with swapped order O<sub>i</sub> O<sub>i</sub> equivalent to S
- If S can become S' via swapping nonconflicting operations
  - S is conflict serializable



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$T_0$	$T_1$
read(A)	
write(A)	
	read(A)
	write(A)
read(B)	
write(B)	
	read(B)
	write(B)

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#### **Locking Protocol**

- Ensure serializability by associating lock with each data item
  - Follow locking protocol for access control
- Locks
  - Shared T<sub>i</sub> has shared-mode lock (S) on item Q, T<sub>i</sub> can read Q but not write Q
  - Exclusive Ti has exclusive-mode lock (X) on Q, T<sub>i</sub> can read and write Q
- Require every transaction on item Q acquire appropriate lock
- If lock already held, new request may have to wait
  - Similar to readers-writers algorithm



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#### **Two-phase Locking Protocol**

- Generally ensures conflict serializability
- Each transaction issues lock and unlock requests in two phases
  - Growing obtaining locks
  - Shrinking releasing locks
- Does not prevent deadlock



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#### **Timestamp-based Protocols**

- Select order among transactions in advance timestamp-ordering
- Transaction T<sub>i</sub> associated with timestamp TS(T<sub>i</sub>) before T<sub>i</sub> starts
  - TS(T<sub>i</sub>) < TS(T<sub>i</sub>) if Ti entered system before T<sub>i</sub>
  - TS can be generated from system clock or as logical counter incremented at each entry of transaction
- Timestamps determine serializability order
  - If TS(T<sub>i</sub>) < TS(T<sub>j</sub>), system must ensure produced schedule equivalent to serial schedule where T<sub>i</sub> appears before T<sub>i</sub>



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# Timestamp-based Protocol Implementation

- Data item Q gets two timestamps
  - $\bullet$  W-timestamp(Q) largest timestamp of any transaction that executed write(Q) successfully
  - R-timestamp(Q) largest timestamp of successful read(Q)
  - Updated whenever read(Q) or write(Q) executed
- Timestamp-ordering protocol assures any conflicting read and write executed in timestamp order
- Suppose Ti executes read(Q)
  - If TS(T<sub>i</sub>) < W-timestamp(Q), Ti needs to read value of Q that was already overwritten
    - read operation rejected and T<sub>i</sub> rolled back
  - If  $TS(T_i) \ge W$ -timestamp(Q)
    - read executed, R-timestamp(Q) set to max(R-timestamp(Q), TS(T₁))



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# **Timestamp-ordering Protocol**

- Suppose Ti executes write(Q)
  - If TS(T<sub>i</sub>) < R-timestamp(Q), value Q produced by T<sub>i</sub> was needed previously and T<sub>i</sub> assumed it would never be produced
    - ▶ Write operation rejected, T<sub>i</sub> rolled back
  - If TS(T<sub>i</sub>) < W-tilmestamp(Q), T<sub>i</sub> attempting to write obsolete value of Q
    - ▶ Write operation rejected and T₁ rolled back
  - Otherwise, write executed
- Any rolled back transaction T<sub>i</sub> is assigned new timestamp and restarted
- Algorithm ensures conflict serializability and freedom from deadlock



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# Schedule Possible Under Timestamp Protocol

$T_2$	$T_3$
read(B)	
	read(B)
	write(B)
read(A)	
	read(A)
	write(A)



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