國立臺灣大學電資學院電機學群開授課程大綱格式

所別:電子所	組別:ICS	修習年級:大四、研究所	每週演講時數:3
課程	☑1.一般性課程(含必修、選修)☑3.教育學程☑4.軍訓		2)社會 (3)物質 (4)生命
	課號: EEE5004		學分:3
	中文名稱:高等類比積體電路	 英文名稱:Advanced Analog Integ	rated Circuit
授課教師	陳信樹		
課程大綱 一	內容 In this course students study the design of mixed-signal circuits for implementing the interfaces between analog and digital signals in CMOS VLSI systems. The course begins with an overview of data conversion system applications, performance metrics and architectures, followed by an examination of CMOS operational amplifiers, sample-and-hold circuits, comparators, and gain blocks. It would also include data converter testing techniques. The course would focus on the design of Nyquist-rate and oversampling A/D and D/A converters. 1 Fundamentals of data converters 2 Sampling Circuits 3 Comparators 4 Nyquist-rate ADC designs 5 Nyquist-rate ADC designs 6 Oversampling delta-sigma ADC and DAC designs — 教科書 "Analog Integrated Circuit Design" by T. C. Carusone, D. Johns and K. Martin, 2nd ed., Wiley 2013. "Principles of Data Conversion System Design" by Behzad Razavi, IEEE Press, 1995. "Understanding Delta-Sigma Data Converters" by R. Schreier and G. C. Temes, 2004 — 成績評量方式		
	Homework (20%) + One Examination (30%) + Project (50%)		
	四 建議預修課程		
	Analog IC Design, Microelectronics, Electric Circuits		
更新日期	107 年 9 月	5 日	