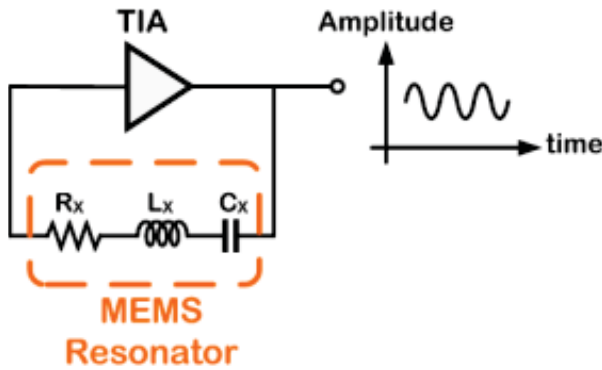


# CMOS PHASE-LOCKED LOOP WITH INTEGRATED MEMS RESONATOR



## PFD, Charge Pump

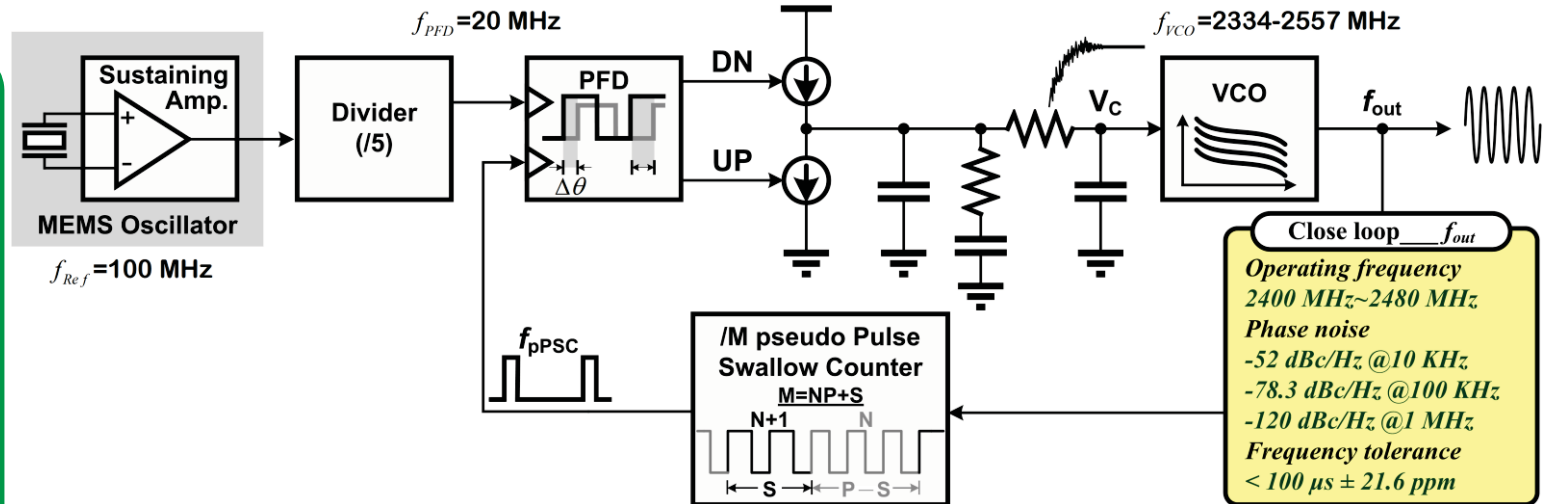
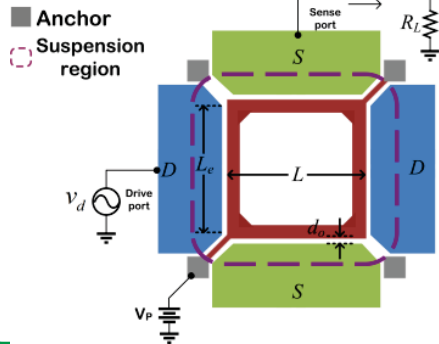
The type-IV PFD, we chosen that because of its detecting range covers  $\pm 2\pi$ . Our CP has high linearity. The dead zone, PFD+Charge Pump, had eliminated.

## VCO

The loss of inductor was compensated by cross-couple pairs, the wide tune and the fine tune were controlled through  $V_b$  and  $V_c$ , respectively.

## Lamé mode resonator

The anchors of Lamé mode resonator move away from main structure, different from C-C Beam, that suppresses energy loss through them.



Close loop  $f_{out}$   
 Operating frequency  
 2400 MHz~2480 MHz  
 Phase noise  
 -52 dBc/Hz @10 KHz  
 -78.3 dBc/Hz @100 KHz  
 -120 dBc/Hz @1 MHz  
 Frequency tolerance  
 < 100  $\mu$ s  $\pm$  21.6 ppm

## Ring oscillator topology

The oscillator uses MEMS resonator as its frequency selective tank, there will be oscillate if the loop gain  $L(j\omega)$  at  $\omega_o$  matched the Barkhausen's Criteria.

## pseudo Pulse Swallow Counter

We applied a adjustable pulse width unit to replace S-counter, that has the same performance and more simple than the traditional PSC.

