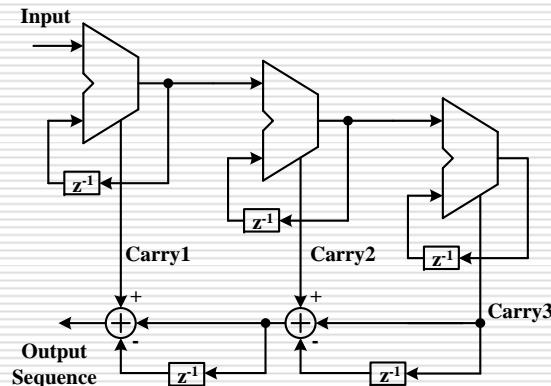


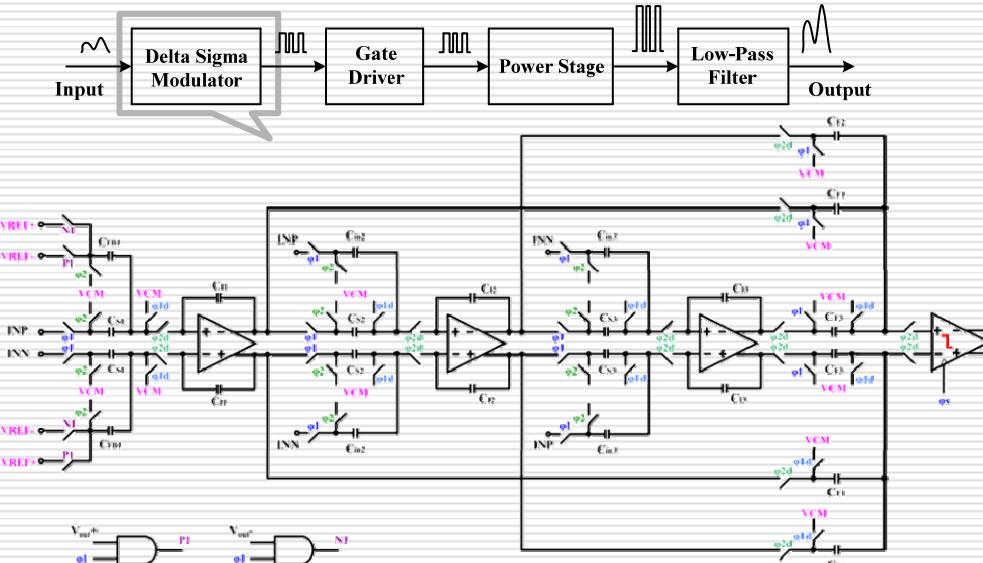
## I. Digital DSM for Fractional-N Synthesizer:



### **Simulation Result:**

Tech.	TSMC rf-018
<b>Resolution</b>	$1/2^{16}$
<b>Input Bits</b>	16
<b>Output Bits</b>	4 (with 1 sign bit)
<b>Operation Freq.</b>	80 MHz
<b>Supply Voltage</b>	1.2V
<b>Architecture</b>	MASH 1-1-1
<b>Order</b>	3
<b>Power Composition</b>	970 $\mu$ W

## II. DSM for Class-D Audio Amplifier:



## Simulation Result:

	Condition	Simulation Result
Max. Input Voltage		3.3 V
Max. Output Power	Output Power @ 1kHz	343 mW
Quiescent Current	Worst Case	6.23 mA
SNR	Max. Value	96.40 dB
SINR	Max. Value	73.89 dB
THD+N	Max. Output Power	0.064 %
Efficiency	Max. Output Power	89.6 %