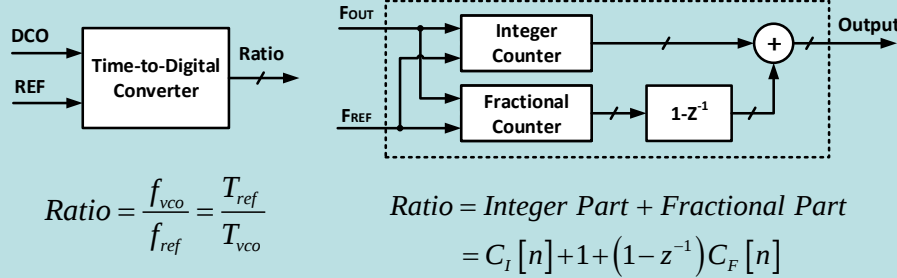


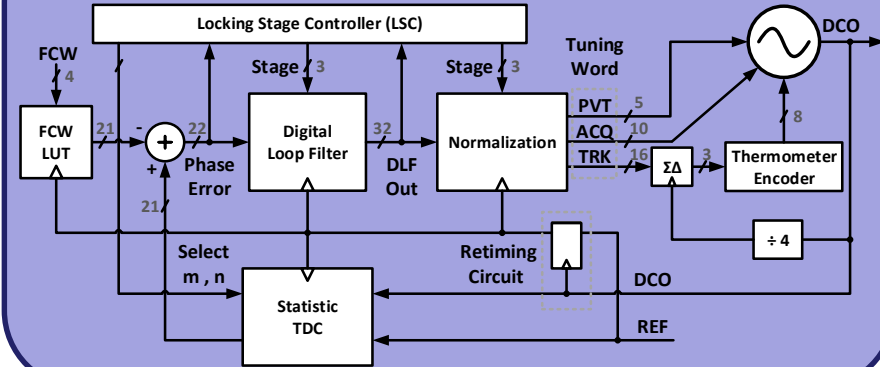
An All-Digital Frequency Synthesizer with Statistic TDC



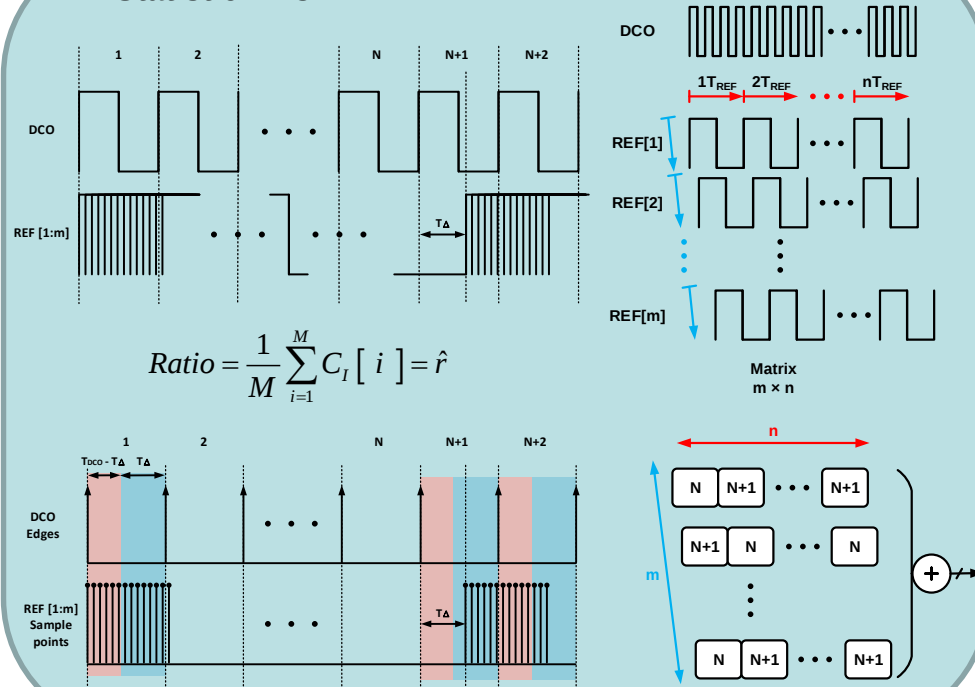
TDC Design



Proposed ADPLL



Statistic TDC



Chip & PCB

