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EDUCATIONAL BACKGROUND:

Ph.D. Computer Science (August, 1986)
University of Texas at Austin, USA
M.S. Computer Engineering (June, 1982)
National Chiao-Tung University, Taiwan
B.S. Electrical Engineering (June, 1980)
National Taiwan University, Taiwan

EMPLOYMENT HISTORY:

1991.8-present:
Professor, Department of Electrical Engineering,
National Taiwan University, Taiwan, R.O.C.

1990.8-1991.7:
Associate Professor, Department of Electrical Engineering,
National Taiwan University, Taiwan, R.O.C.

1986.8-1990.7:
Assistant Professor, Department of Computer Science,
Iowa State University, U.S.A.

August, 1987, July, 1988:
Research Fellow, Department of Computer Sciences,
The University of Texas at Austin, U.S.A.

1983-1986:
Research Assistant, Teaching Assistant, Department of Computer Sciences,
The University of Texas at Austin, U.S.A.

Additional employment history information available upon request.

PROFESSIONAL ACTIVITIES:

Journal Editorships:

Editor: International Journal of Foundations of Computer Science (IJFCS)

Editor: Journal of Information Science and Engineering (JISE) 2000 - 2005.

Editor: Science Development, National Science Council, Taiwan.

Guest Editor: Special issue on 'Verification and Analysis of Infinite State Systems', International Journal of Foundations of Computer Science, Vol. 14, No. 4, August 2003 (co-edit with Farn Wang)

Guest Editor: Special issue on 'Graph Drawing', International Journal of Foundations of Computer Science, 2006 (co-edit with Seokhee Hong)

Service to Conferences (2002 - present):

Steering Committee Member: ATVA (International Workshop on Automated Technology for Verification and Analysis); Other members include E. Allen Emerson, Oscar H. Ibarra, Doron A. Peled, Insup Lee, and Farn Wang.

PC Co-Chair: 11th International Conference on Implementation and Application of Automata (CIAA 2006).

PC Member: International Conference on Developments in Language Theory, DLT 2006.

PC Member: International Symposium on Automated Technology for Verification and Analysis, ATVA 2006, ATVA 2005, ATVA 2004, ATVA 2003.

PC Member: International Conference on Implementation and Application of Automata, CIAA 2006, CIAA 2005, CIAA 2004, CIAA 2003.

PC Member: IEEE Symposium on Pacific Rim Dependable Computing; PRDC 2006, PRDC 2005.

PC Member: Asia Pacific Symposium on Information Visualization, APVIS 2006, APVIS 2005.

PC Member: IFIP Formal Techniques for Networked and Distributed Systems, FORTE 2005.

PC Member: International Symposium on Algorithms and Computation, ISAAC 2004.

PC Member: International Computing and Combinatorics Conference, CoCoon 2002.

Awards:

Special Researcher Award (2002-2007), National Science Council, Taiwan.

Distinguished Research Award (1996-2001), National Science Council, Taiwan.

Teaching Awards (2005, 2004, 2003, 1993, 1992), National Taiwan University.

Others

Instructor: Short course (*Petri net theory and its applications*) for International PhD School in Formal Languages and Applications, Rovira i Virgili University, Tarragona, Spain. (July 8-11, 2005, and July 20-21, 2006)

AREAS of RESEARCH INTEREST:

Theory of Computation

Design and Analysis of Algorithms

Petri Nets

Graph Drawing

Formal Methods

PUBLICATIONS:

Journal Publications:

1. Decidability Analysis of Self-Stabilization for Infinite State Systems, (H. Yen and L. Yu), **Fundamenta Informaticae**, Vol. 70, No. 4, 387-402, 2006.
2. On Various Notions of Parallelism in P Systems, (O. Ibarra, H. Yen, and Z. Dang), **International Journal of Foundations of Computer Science**, Vol. 16, No. 4, pp. 683-706, August 2005.
3. Quality-of-Service Provisioning System for Multimedia Transmission in IEEE 802.11 Wireless LANs, (D. Deng, and H. Yen), **IEEE Journal on Selected Areas in Communications** (special issue on Mobile Computing and Networking), Vol. 23, No. 6, pp. 1240-1252, June 2005.
4. Reachability Solution Characterization of Parametric Real-time Systems, (F. Wang and H. Yen), **Theoretical Computer Science**, Vol. 328, pp. 187-201, 2004.
5. Compact Floor-Planning via Orderly Spanning Trees, (C. Liao, H. Lu and H. Yen), **Journal of Algorithms**, Vol. 48, No. 2, pp. 441-451, September 2003.
6. An Omega-Automata Approach to the Representation of Bi-level Images, (K. Lin and H. Yen), **IEEE Trans. on Systems, Man, and Cybernetics (Part B: Cybernetics)**, Vol. 33, No. 3, pp. 524-531, June 2003.
7. Sequential versus Concurrent Languages of Labeled Conflict-Free Petri Nets, (H. Yen), **IEEE Trans. on Automatic Control**, Vol. 47, No. 7, pp. 1158-1162, July 2002.
8. A Valuation-Based Analysis of Conflict-Free Petri Nets, (H. Yen), **Systems and Control Letters**, Vol. 45, No. 5, pp. 387 - 395, 2002.
9. Distributed and On-line Routing on Tori, (T. Yeh, C. Kuo, C. Lei, and H. Yen), **Algorithmica**, Vol. 32, pp. 562-593, 2002.
10. The Symmetry Number Problem for Trees, (K. Chin, and H. Yen), **Information Processing Letters**, Vol. 79, No. 2, pp. 73-79, May 2001.
11. Integer Linear Programming and the Analysis of Some Petri Net Problems, (H. Yen), **Theory of Computing Systems** (formerly, **Mathematical Systems Theory**), Vol. 32, No. 4, pp. 467-485, 1999.
12. Priority Conflict-Free Petri Nets, (H. Yen), **Acta Informatica**, Vol. 35, No. 8, pp. 673-688, 1998.

13. Competitive Analysis of On-Line Disk Scheduling, (T. Yeh, C. Kuo, C. Lei and H. Yen), **Theory of Computing Systems** (formerly, **Mathematical Systems Theory**), Vol. 31, No. 5, pp. 491-506, 1998.
14. Deciding a Class of Path Formulas for Conflict-Free Petri Nets, (H. Yen, B. Wang, and M. Yang), **Theory of Computing Systems** (formerly, **Mathematical Systems Theory**) Vol. 30, No. 5, pp. 475-494, September/October 1997.
15. On Reachability Equivalence for BPP Nets, (H. Yen), **Theoretical Computer Science**, Vol. 179, No. 1-2, pp. 397-419, June 1997.
16. Deciding Bisimulation and Trace Equivalences for Systems with Many Identical Processes, (H. Yen, S. Jian, and T. Lao), **Theoretical Computer Science**, Vol. 170, No. 1-2, pp. 445-464, December 1996.
17. On the Regularity of Petri Net Languages, (H. Yen), **Information and Computation**, Vol. 124, No. 2, pp. 168-181, 1996.
18. A Note on Fine Covers and Iterable Factors of VAS Languages, (H. Yen), **Information Processing Letters**, Vol. 56, No. 5, pp. 237-243, December 1995.
19. Some Complexity Results for Rings of Petri Nets, (H. Yen, B. Wang, and M. Yang), **International Journal of Foundations of Computer Science**, Vol. 5, Nos. 3 & 4, pp. 281-292, 1994.
20. On Multiterminal Single Bend Wirability, (H. Yen), **IEEE Transactions on Computer Aided Design of Integrated Circuits and Systems**, Vol. 13, No. 6, pp. 822-826, June 1994.
21. Complexity Analysis of Propositional Concurrent Programs Using Domino Tiling, (H. Yen and N. Pak), **Mathematical Systems Theory**, Vol. 26, pp. 357-378, 1993.
22. Normal and Sinkless Petri Nets, (R. Howell, L. Rosier, and H. Yen), **Journal of Computer and System Sciences**, Vol. 46, pp. 1-26, February 1993.
23. A Multiparameter Analysis of Domino Tiling with an Application to Concurrent Systems, (H. Yen), **Theoretical Computer Science**, Vol. 98, pp. 263-287, 1992.
24. A Unified Approach for Deciding the Existence of Certain Petri Net Paths, (H. Yen), **Information and Computation**, Vol. 96, No. 1, pp. 119-137, 1992.
25. Priority Systems with Many Identical Processes (H. Yen), **Acta Informatica**, Vol. 28, pp. 681-692, 1991.
26. A Polynomial Time Algorithm to Decide Pairwise Concurrency of Transitions for 1-Bounded Conflict-Free Petri Nets, (H. Yen), **Information Processing Letters**, Vol. 38, No. 2, pp. 71-76, 1991.
27. A Taxonomy of Fairness and Temporal Logic Problems for Petri Nets, (R. Howell, L. Rosier and H. Yen), **Theoretical Computer Science**, Vol. 82, 1991, pp. 341-372.
28. Global and Local Views of State Fairness, (R. Howell, L. Rosier and H. Yen), **Theoretical Computer Science**, Vol. 80, No. 1, 1991, pp. 77-104.

29. On Optimal Parallelization of Sorting Networks (E. Gannett, S. Kothari and H. Yen), **Theoretical Computer Science**, Vol. 76, No. 2-3, 1990, pp. 331-341.
30. Communicating Processes, Scheduling and the Complexity of Nontermination, (H. Yen), **Mathematical Systems Theory**, Vol. 23, No. 1, 1990, pp. 33-59.
31. Boundedness, Hierarchy of Fairness and Communication Networks with Delay, (A. Sharma and H. Yen), **International Journal of Computer Mathematics**, Vol. 26, 1989, pp. 161-178.
32. On the Complexity of Deciding Fair Termination of Probabilistic Concurrent Finite-State Programs (L. Rosier and H. Yen), **Theoretical Computer Science**, Vol. 58, No. 1-3, 1988, pp. 263-324.
33. Logspace Hierarchies, Polynomial Time and the Complexity of Fairness Problems Concerning ω -Machines (L. Rosier and H. Yen), **SIAM J. on Computing**, Vol. 16, No. 5, 1987, pp. 779-807.
34. An $O(n^{1.5})$ Algorithm to Decide Boundedness for Conflict-Free Vector Replacement Systems (R. Howell, L. Rosier and H. Yen), **Information Processing Letters**, Vol. 25, No. 1, 1987, pp. 27-33.
35. Some Complexity Bounds for Problems Concerning Finite and 2-Dimensional Vector Addition Systems with States (R. Howell, D. Huynh, L. Rosier and H. Yen), **Theoretical Computer Science**, Vol. 46, No. 2,3, 1986, pp. 107-140.
36. Boundedness, Empty Channel Detection, and Synchronization for Communicating Finite Automata (L. Rosier and H. Yen), **Theoretical Computer Science**, Vol. 44, No. 1, 1986, pp. 69-105.
37. A Multiparameter Analysis of the Boundedness Problem for Vector Addition Systems (L. Rosier and H. Yen), **Journal of Computer and System Sciences**, Vol. 32, No. 1, 1986, pp. 105-135.

Conference Publications:

1. Mental Map Preserving Graph Drawing Using Simulated Annealing, (Y. Lee, C. Lin, and H. Yen), in **Proc. of the Asia Pacific Symposium on Information Visualisation 2006** (APVIS 2006), Tokyo, Japan, Feb. 1-3, 2006.
2. On Balloon Drawings of Rooted Trees, (C. Lin, and H. Yen), in **Proc. of the 13th International Symposium on Graph Drawing** (GD 2005), (LNCS 3843), Sept. 12-14, 2005, Limerick, Ireland.
3. On Symport/Antiport Systems and Semilinear Sets, (O. Ibarra, S. Woodworth and H. Yen, Z. Dang), in **Proc. of the 6th International Workshop on Membrane Computing** (WMC6), (LNCS 3850), July 18 - 21, 2005, Vienna, Austria.
4. On Deterministic Catalytic Systems, (O. Ibarra, and H. Yen), in **Proc. of the 10th International Conference on Implementation and Application of Automata** (CIAA 2005), (LNCS 3845), June 27 - 29, 2005, Sophia Antipolis, France.
5. On Sequential and 1-Deterministic P Systems, (O. Ibarra, S. Woodworth, H. Yen and Z. Dang), in **Proc. of the 11th International Computing and Combinatorics Conference** (COCOON 2005), (LNCS 3595), August 16 - 19, Kunming, Yunnan, China.

6. Signaling P Systems and Verification Problems, (C. Li, Z. Dang, O. Ibarra, and H. Yen), in **Proc. of the 32nd International Colloquium on Automata, Languages and Programming (ICALP'05)**, (LNCS 3580), pp. 1462-1473, July 11-15, 2005, Lisboa, Portugal.
7. A new force-directed graph drawing method based on edge-edge repulsion, (C. Lin and H. Yen), in **Proc. of the 9th International Conference on Information Visualisation (IV 2005)**, July 6-8, 2005, London, UK.
8. Design and Performance Evaluation of an Optimal Collision Avoidance Mechanism over Congested and Noisy Channels for IEEE 802.11 DCF Access Method, (D. Deng and H. Yen), in **Proc. of International Conference on Information Networking 2005**, (ICOIN 2005) (LNCS 3391), pp. 11-20, Jan. 31- Feb. 2, 2005, Jeju Korea.
9. The power of maximal parallelism in P systems, (O. Ibarra, H. Yen, and Z. Dang), **Proc. of Eighth International Conference on Developments in Language Theory (DLT'04)** (LNCS 3340), pp. 212-224, Dec. 13-17, 2004, Auckland, New Zealand.
10. Dependability Analysis of a Class of Probabilistic Petri Nets, (H. Yen and L. Yu), in **Proc. of 10th IEEE Pacific Rim Int'l Symp. on Dependable Computing**, (PRDC 2004) pp. 373-382, March 3-5, 2004, Tahiti, French Polynesia.
11. Modelling and Analysis of Asynchronous Circuits and Timing Diagrams Using Parametric Timed Automata, (C. Chen, T. Lin, and H. Yen), in **Proc. of the 23rd IASTED Int'l Conf. on Modelling, Identification and Control (MIC 2004)**, February 23-25, 2004 Grindelwald, Switzerland.
12. Quality-of-Service Provisioning System for Multimedia Transmission in IEEE 802.11 Wireless LANs, (D. Deng, and H. Yen), in **Proc. of the IASTED Int'l Conf. on Parallel and Distributed Computing and Networks (PDCN 2004)**, February 17-19, 2004 Innsbruck, Austria.
13. Drawing Graphs with Non-Uniform Nodes Using Potential Fields, (J. Chuang, C. Lin, and H. Yen), in **Proc. of 11th International Symposium on Graph Drawing (GD 2003)** (LNCS 2912) pp. 460-465, September 21-24, 2003, Perugia, Italy.
14. Timing Parameter Characterization of Real-Time Systems, (F. Wang and H. Yen), in the **Proceedings of the 8th International Conference on Implementation and Application of Automata (CIAA 2003)**, (LNCS 2759) pp. 23-34, July 16-18, 2003, Santa Barbara, CA, USA.
15. Petri Nets with Simple Circuits, (H. Yen, and L. Yu), in the **Proceedings of the Ninth International Computing and Combinatorics Conference**, (COCOON 2003) (LNCS 2697), pp. 149-158, July 25-28, 2003, Big Sky, MT, USA.
16. Some Applications of Orderly Spanning Trees in Graph Drawing, (H. Chen, C. Liao, H. Lu and H. Yen), in the **Proceedings of the 10th International Symposium on Graph Drawing (GD 2002)** (LNCS 2528) pp. 332-343, August 26-28, 2002, Irvine, California.
17. A Dictionary-based Compressed Pattern Matching Algorithm, (M. Ho, and H. Yen), in the **Proceedings of the IEEE COMPSAC 2002**, Oxford, England, August 26-29, 2002.
18. On Nearly Symmetric Drawings of Graphs, (M. Chuang, and H. Yen), in the **Proceedings of the 6th International Conference on Information Visualisation**, pp. 489-494, London, England, July 10-12, 2002.

19. Floor-Planning via Orderly Spanning Tree, (C. Liao, H. Lu, and H. Yen), in the **Proceedings of the 9th International Symposium on Graph Drawing 2001**, (LNCS 2265) pp. 367-377, Vienna, Austria, Sept. 23-26, 2001.
20. Parametric Optimization of Open Real-Time Systems, (F. Wang and H. Yen), in the **Proceedings of the 8th International Static Analysis Symposium (SAS 2001)**, (LNCS 2126) pp. 299-318, La Sorbonne, Paris, 16-18 July 2001.
21. Analysis of Self-Stabilization for Infinite-State Systems, (H. Yen), in the **Proceedings of the 7th IEEE International Conference on Engineering of Complex Computer Systems (ICECCS 2001)**, pp. 240-248, Skoevde, Sweden, June 11-13, 2001.
22. On Maximum Symmetric Subgraphs, (H. Chen, H. Lu and H. Yen), in the **Proceedings of the 8th International Symposium on Graph Drawing (GD 2000)**, (LNCS 1984), pp. 372-383, Colonial Williamsburg, Virginia, USA, September 20-23, 2000.
23. Fair Control of Omega-Automata, (H. Yen), in the **Proceedings of the 5th International Workshop on Discrete Event Systems (WODES 2000)** (published in a book 'Discrete Event Systems: Analysis and Control' by Kluwer Academic Publishers), pp. 355-362, Ghent, Belgium, August 21-23, 2000.
24. An Omega-Automata Approach to the Compression of Bi-Level Images, (Y. Lin and H. Yen), in the **Proceedings of CATS 2000 Computing: the Australasian Theory Symposium**, Canberra, Australia, February 1-2, 2000. Electronic Notes in Theoretical Computer Science, Vol. 31, 2000.
25. Orthogonal and Straight-Line Drawings of Graphs with Succinct Representations, (H. Chen, and H. Yen), in the **Proceedings of the 7th International Symposium on Graph Drawing (GD 1999)** (LNCS 1731), pp. 416-417, Stirin Castle, Czech, September 1999.
26. , and H. Yen, Competitive Source Routing on Tori and Meshes, (T. Yeh, C. Kuo, C. Lei), in the **Proceedings of the 8th Annual International Symposium on Algorithms and Computation (ISAAC 1997)** (LNCS 1350), pp. 82-91, Singapore, December 1997.
27. Competitive Analysis of On-Line Disk Scheduling, (T. Yeh, C. Kuo, C. Lei, and H. Yen), in the **Proceedings of the 7th Annual International Symposium on Algorithms and Computation (ISAAC 1996)** (LNCS 1178), pp. 356-365, Osaka, Japan, December 1996.
28. Deciding Bisimulation and Trace Equivalences for Systems with Many Identical Processes, (H. Yen, S. Jian, and T. Lao), in the **Proceedings 6th Annual International Symposium on Algorithms and Computations**, (ISAAC 1995) (LNCS 1004), pp. 301-310, Cairns, Australia, December 1995.
29. Design, Implementation and Application of a B-ISDN Simulation Testbed, (J. Chang, S. Chang, Z. Tsai, J. Wu, and H. Yen), in the **Proceedings of the 28th Annual Simulation Symposium**, pp. 212-219, Phoenix, U.S.A., April 1995.
30. Some Complexity Results for Rings of Petri Nets, (H. Yen, B. Wang, and M. Yang), in the **Proceedings of the 7th International Conference on Parallel and Distributed Computing Systems**, pp. 354-357, Las Vegas, U.S.A., Oct. 1994.
31. On the Regularity of Petri Net Languages, (H. Yen), in the **Proceedings of the 1994 IEEE International Phoenix Conference on Computers and Communications**, pp. 329-335, Phoenix, AZ, U.S.A., April 1994.

32. A Unified Approach for Reasoning about Conflict-Free Petri Nets, (H. Yen, B. Wang, and M. Yang), in the **Proceedings of the 14th International Conference on Application and Theory of Petri Nets**, pp. 513-531, Chicago, U.S.A., June, 1993.
33. Complexity Analysis of Propositional Concurrent Programs Using Domino Tiling, (H. Yen and N. Pak), in the **Proceedings of the 26th Hawaii International Conference on System Sciences**, pp. 253-262, Hawaii, U.S.A., January, 1993.
34. Applications of Conflict-Free Petri Nets to Parallel Programs and Asynchronous Circuits, (H. Yen), in the **Proceedings of the 1992 IEEE International Phoenix Conference on Computers and Communications**, pp. 766-773, Scottsdale, AZ, U.S.A., April 1992.
35. Some Complexity Results for Problems Concerning Ada Programs (N. Pak and H. Yen), in the **Proceedings of the 24th Annual Conference on Information Sciences and Systems**, Princeton, NJ, March 1990.
36. Priority Systems with Many Identical Processes (N. Pak and H. Yen), in the **Proceedings of the 24th Annual Conference on Information Sciences and Systems**, Princeton, NJ, March 1990.
37. A Logic for Petri Nets and Its Applications, (H. Yen), in the **Proceedings of the Twenty-Seventh Annual Allerton Conference on Communication, Control, and Computing**, pp. 714-723, Monticello, Illinois, September 1989.
38. Normal and Sinkless Petri Nets, (R. Howell, L. Rosier and H. Yen), in the **Proceedings of the 7th International Conference on the Fundamentals of Computing Theory**, pp. 234-243, Szeged, Hungary, August 1989.
39. Communicating Processes, Scheduling and the Complexity of Termination Problems, (H. Yen), in the **Proceedings of 1988 International Computer Symposium**, pp. 46-51, Taiwan, Republic of China, December 1988.
40. A Taxonomy of Fairness and Temporal Logic Problems for Petri Nets, (R. Howell, L. Rosier and H. Yen), in the **Proceedings of the 13th Symposium on Mathematical Foundations of Computer Science**, pp. 351-359, Carlsbad-Karlovy Vary, Czechoslovakia, August 1988.
41. Boundedness, Hierarchy of Fairness and Communication Networks with Delay, (A. Sharma and H. Yen), in the **Proceedings of the 22nd Annual Conference on Information Sciences and Systems**, pp. 445-449, Princeton, NJ, March 1988.
42. On Optimal Parallelization of Sorting Networks (E. Gannett, S. Kothari and H. Yen), in the **Proceedings of the 7th Conference on Foundations of Software Technology and Theoretical Computer Science**, pp. 195-206, Pune, India, December 1987.
43. On Exact, Critical and Unique Versions of Some Graph Problems (L. Rosier and H. Yen), in the **Proceedings of the Twenty-fifth Annual Allerton Conference on Communication, Control, and Computing**, pp. 1214-1223, Monticello, Illinois, September 1987.
44. Some Complexity Bounds for Problems Concerning Finite and 2-Dimensional Vector Addition Systems with States (R. Howell, D. Huynh, L. Rosier and H. Yen), in the **Proceedings of the Fourth Annual Symposium on Theoretical Aspects of Computer Science**, pp. 360-370, Passau, Federal Republic of Germany, February 1987.

45. On Global vs. Local State Fairness, (R. Howell, L. Rosier and H. Yen), in the **Proceedings of the Twenty-Fourth Annual Allerton Conference on Communication, Control, and Computing**, pp. 721-729, Monticello, Illinois, October 1986.
46. On the Complexity of Deciding Fair Termination of Probabilistic Concurrent Finite-State Programs, (L. Rosier and H. Yen), in the **Proceedings of 13th International Colloquium on Automata, Languages and Programming**, pp. 334-343, Rennes, France, July 1986.
47. Logspace Hierarchies, Polynomial Time and the Complexity of Fairness Problems Concerning ω -Machines (L. Rosier and H. Yen), in the **Proceedings of the Third Annual Symposium on Theoretical Aspects of Computer Science**, pp. 306-320, Orsay, France, January 1986.
48. A Theory of Closed Covers - To Decide Progress of Communicating Finite State Machines (M. Gouda, L. Rosier and H. Yen), in the **Proceedings of the Twenty-Third Annual Allerton Conference on Communication, Control, and Computing**, pp. 914-922, Monticello, Illinois, October 1985.
49. A Multiparameter Analysis of the Boundedness Problem for Vector Addition Systems (L. Rosier and H. Yen), in the **Proceedings of the Fifth International Conference on the Fundamentals of Computation Theory**, pp. 361-370, Cottbus, German Democratic Republic, September 1985.
50. Boundedness, Empty Channel Detection and Synchronization for Communicating Finite State Machines (L. Rosier and H. Yen), in the **Proceedings of the Second Annual Symposium on Theoretical Aspects of Computer Science**, pp. 287-298, Saarbrücken, West-Germany, January 1985.
51. CTIPS – A Multi-microprocessor system for Image Processing: Hardware Architecture, System Software, and Applications (W. Tsai, B. Jeng, C. Chou, G. Char, P. Liu, C. Wang, and H. Yen), in the **Proceedings of 1983 International Symposium on VLSI Technology, Systems, and Applications**, pp. 235-239, Taiwan, Republic of China, March 1983.
52. Moving Target Detection and Tracking on a Multi-microprocessor System for Image Processing (W. Tsai, C. Wang and H. Yen), in the **Proceedings of 1982 International Computer Symposium**, pp. 292-298, Taiwan, Republic of China, December 1982.