## CODING AND ANALYSIS SUBSYSTEMS OF DISTRIBUTED VIDEO SENSORS

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## OUTLINE

- Background
- Coding subsystem of distributed video sensors
- Analysis subsystem of distributed video sensors
- Discussion

## BACKGROUND

#### Newly introduced technologies

- Cloud computing
- M2M network
- Internet-of-Things
- Advanced computer vision/pattern recognition/machine learning technologies
- Large-scale data analysis



## BACKGROUND

 Billions of objects or machines are connected and interact with each other without human intervention



Intel: 15 billion connected and intelligent devices by 2015

## BACKGROUND

 The future of wireless technologies from Berkeley Wireless Research Center (BWRC)

## The Birth of Societal IT Systems\*:

Looking Beyond the Devices







Complex collections of sensors, controllers, compute and storage nodes, and actuators that work together to improve our daily lives





\*Also known as SiS







## NTU'S VISION ON M2M: CAT

- Machines that collaboratively
  - Capture the data from surroundings
  - Analyze the collected information
  - Take appropriate action
  - before human reaction

Machines that talk to each other and collaborate with each other

## DISTRIBUTED VIDEO SENSORS

- The growth of distributed video sensor deployment
  - Surveillance camera
  - Mobile phones
  - Video sensors on cars
  - Distributed sensors
- M2M network with video sensor nodes -- Eyes of M2M Networks
- How to

store/access/analysis this large amount of content?



### CODING AND ANALYSIS SUBSYSTEMS OF DISTRIBUTED VIDEO SENSORS

## SIGGSP



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## VIDEO SENSOR NODE FOREVER

An improvement of 3X power efficiency is expected to enable perpetual video sensor nodes

## POSITION IN SIGGSP

#### Intelligent Sensor Node (iSensor) Structure



## EYES OF M2M NETWORKS

M2M network with video sensor nodes

![](_page_12_Figure_2.jpeg)

 Low power video coding and analysis techniques are the target of this project

## CODING SUBSYSTEM OF DISTRIBUTED VIDEO SENSORS

## COMPRESSION IS NECESSARY!

- For 640x480 RGB 30fps video from 10 video sensors
  - 640x480x24x30x10=2.2Tbps!

## IMAGE SEQUENCE MODEL

![](_page_15_Figure_1.jpeg)

## CONVENTIONAL HYBRID VIDEO CODING PROCESS

![](_page_16_Figure_1.jpeg)

## BASIC VIDEO CODING FLOW

![](_page_17_Figure_1.jpeg)

## DECODING BLOCK DIAGRAM

#### Decoder

![](_page_18_Figure_2.jpeg)

Codec=Encoder+Decoder

## STAGE 1 -

## REDUCING TEMPORAL REDUNDANCY

- Segment a frame into macroblocks
- Compensate motion and remove temporal redundancy
- Output energy is related to the degree of temporal redundancy

## STAGE 2 -REDUCING SPATIAL REDUNDANCY

- Processing the difference frame (spatially correlated) from stage 1
- Usually using DCT coding
- This stage is Intra-frame coder
- The method by these two stages is Hybrid coding method

## CONVENTIONAL VIDEO CODING

#### 

![](_page_21_Figure_2.jpeg)

Ref: Shao-Yi Chien, Yu-Wen Huang, Ching-Yeh Chen, Homer H. Chen, and Liang-Gee Chen, "Hardware architecture design of video compression for multimedia communication systems," *IEEE Communications Magazine*, vol. 43, no. 8, pp. 122–131, Aug. 2005.

## CHARACTERISTICS OF CONVENTIONAL VIDEO CODING SYSTEMS

- Good coding performance
- Complex encoder and simple decoder
- Close-loop coding system
- Not robust over noisy channel

• Suitable for M2M networks?

## NEW PARADIGM ---DISTRIBUTED VIDEO CODING (DVC)

- Distributed compression refers to the coding of two (or more) dependent random sequences.
- Special case of distributed video coding
  - Compression with the side information

![](_page_23_Figure_4.jpeg)

## FUNDAMENTAL OF DISTRIBUTED SOURCE CODING

- Slepian-Wolf Theorem
  - Separate convention encoder
    - Rx  $\geq$  H(X) , Ry  $\geq$  H(Y)
  - With jointly decoder
    - $Rx + Ry \ge H(X,Y)$
    - o  $Rx \geqq H(X \,|\, Y)$  ,  $Ry \geqq H(Y \,|\, X)$

![](_page_24_Figure_7.jpeg)

## SOURCE CODING METHOD

- Channel Coding
  - LDPC , Turbo Code
- - X: source, Y: side information
  - We use systematic channel code to generate parity bit to protect X
  - Treat Y as the received signal with noise
  - Perform error-correction decoding
- The compression is achieved because only the parity bits of the error correction codes are sent to the decoder

#### Wyner-Ziv & Slepian-Wolf Coding

- Slepian-Wolf Coding
  - Channel coding (turbo code , LDPC)
  - Encoder only transmits the parity bits to decoder

![](_page_26_Figure_5.jpeg)

- Pixel-Domain Encoding
  - Key frame
    - Coded using conventional intra frame
  - Wyner-Ziv frame
    - $\bullet\,$  Each pixel is uniform quantized with  $2^{M}$  intervals
    - Intra frame coded but Inter frame decoded
  - Splepian-Wolf coder
    - Rate-Compatible Punctured Turbo code (RCPT)
    - Request-and-decode process

#### Pixel-Domain Encoding – Block diagram

![](_page_28_Figure_2.jpeg)

Ref: B. Girod, A.M. Aaron, S. Rane, D. Rebollo-Monedero, "Distributed Video Coding," *Proceedings of the IEEE*, vol.93, no.1, pp.71-83, Jan. 2005

29

#### Transform-Domain Encoding

- Conventional coding
  - Transform spatial data into spectral data
    - Ex: DCT , KLT , Wavelet ,etc
- Perform blockwise DCT to Wyner-Ziv frame
  - Decoder would get side information (spectral) from previous frames
  - A bank of turbo decoders reconstructed the qauntized coefficient bands
  - Each coefficient band is reconstructed with the side information

Transform-Domain Encoding

![](_page_30_Figure_2.jpeg)

## STATE-OF-THE-ART DVC: DISCOVER CODEC

![](_page_31_Figure_1.jpeg)

32

![](_page_32_Picture_0.jpeg)

![](_page_32_Figure_1.jpeg)

Ref: Chieh-Chuan Chiu, Shao-Yi Chien, Chia-han Lee, V. Srinivasa Somayazulu, and Yen-Kuang Chen, "Distributed video coding: a promising solution for distributed wireless video sensors or not?" in *Proc. Visual Communications and Image Processing 2011*, Nov. 2011.

## ANALYSIS OF EXISTING DVC SYSTEMS

- Analysis environment
  - DISCOVER codec
    - Improved frame interpolation with spatial motion smoothing
    - Online correlation noise modeling
    - LDPCA for syndrome coding
  - Conditions
    - Sequences: Foreman, Coastguard, and Hall Monitor
    - Resolution: CIF at 30Hz
    - Q tables from DISCOVER
    - GOP is 2 for DVC, and GOP is 30 for H.264/AVC SP

## RATE-DISTORTION PERFORMANCE

#### Foreman

![](_page_34_Figure_2.jpeg)

# RATE-DISTORTION PERFORMANCE● Hall Monitor

![](_page_35_Figure_1.jpeg)

### 

![](_page_36_Figure_1.jpeg)

## POWER CONSUMPTION ANASLYSIS

- System power consumption is modeled as
  - P<sub>total</sub>=P<sub>t</sub>+P<sub>c</sub>+P<sub>s</sub>, where P<sub>t</sub>: transmission power, P<sub>c</sub>: coding power, and P<sub>s</sub>: sensor power

#### • Two test platforms:

- ASIC-based solution in 65nm technology
- Processor-based solution with Intel ATOM Z530 processor

## PRELIMINARY POWER ANALYSIS RESULTS

• ASIC-based sensor node

![](_page_38_Figure_2.jpeg)

## PRELIMINARY POWER ANALYSIS RESULTS

• Processor-based sensor node

![](_page_39_Figure_2.jpeg)

### MUCH BETTER ERROR ROBUSTNESS

![](_page_40_Figure_1.jpeg)

Ref: R. Puri, A. Majumdar, P.Ishwar, and K. Ramchandran, "Distributed Video Coding in Wireless Sensor Networks," *IEEE Signal Processing Magazine*, July, 2006

## ANALYSIS SUBSYSTEM OF DISTRIBUTED VIDEO SENSORS

## VIDEO ANALYSIS ALGORITHMS

**Background Model** 

![](_page_42_Picture_2.jpeg)

![](_page_42_Picture_3.jpeg)

![](_page_42_Picture_4.jpeg)

![](_page_42_Picture_5.jpeg)

Rank 1 Rank

Rank 61 Rank 62 43

## VIDEO ANALYSIS ALGORITHMS

![](_page_43_Picture_1.jpeg)

#### 2006年旺宏金矽獎優等

![](_page_44_Figure_0.jpeg)

## REDUCE THE TRAFFIC LOAD WITH SEMANTIC LEVELS

Frame # 1116

![](_page_45_Figure_1.jpeg)

Frame # 660

Ref: Shao-Yi Chien and Wei-Kai Chan (2011). Cooperative Visual Surveillance Network with Embedded Content Analysis Engine, Video Surveillance, Available from: http://www.intechopen.com/articles/show/tit le/cooperative-visual-surveillance-networkwith-embedded-content-analysis-engine

46

## VIDEO ANALYSIS ALGORITHM FOR SINGLE CAMERA

![](_page_46_Figure_1.jpeg)

Ref: Shao-Yi Chien and Wei-Kai Chan (2011). Cooperative Visual Surveillance Network with Embedded Content Analysis Engine, *Video Surveillance*, Available from: http://www.intechopen.com/articles/show/tit le/cooperative-visual-surveillance-networkwith-embedded-content-analysis-engine

## WHAT WE HAVE DONE

#### Algorithm/hardware architecture design for single smart camera

![](_page_47_Figure_2.jpeg)

![](_page_47_Figure_3.jpeg)

[CICC2011] (1157.82 GOPS,197 mW @90nm process)

**Fixed Cameras** 

![](_page_48_Figure_2.jpeg)

Ref: Chih-Chun Chia, Wei-Kai Chan, and Shao-Yi Chien, "Cooperative surveillance system with fixed camera object localization and mobile robot target tracking," in *Proc. Pacific Rim Symposium on Advances in Image and Video Technology (PSIVT 2009)*, pp. 886 - 897, Tokyo, Japan, Jan. 2009.

![](_page_49_Figure_1.jpeg)

![](_page_50_Picture_1.jpeg)

![](_page_50_Picture_2.jpeg)

![](_page_50_Picture_3.jpeg)

(c)

![](_page_50_Picture_5.jpeg)

![](_page_51_Picture_1.jpeg)

(b)

![](_page_51_Picture_3.jpeg)

(c)

![](_page_51_Picture_5.jpeg)

(d)

![](_page_51_Picture_7.jpeg)

![](_page_51_Picture_8.jpeg)

![](_page_52_Figure_1.jpeg)

![](_page_52_Picture_2.jpeg)

(c)

![](_page_52_Picture_4.jpeg)

(d)

## SUMMARY

## SUMMARY

- Our target: distributed video sensor nodes with low power consumption
- DVC seems a promising solution for the coding subsystems of distributed video sensors
  - It surprisingly shows that the power consumption of DVC-based systems is not as low as people usually thought
  - The R-D performance of DVC should be further improved
- Better system efficiency can be achieved with integrated video analysis engine
  - How to distribute the workload of video analysis?
- Trade-off analysis plays an important role for this project

## DISCUSSIONS

![](_page_56_Picture_0.jpeg)

#### • Possible applications with distributed video sensors?

![](_page_56_Figure_2.jpeg)

![](_page_57_Picture_0.jpeg)

#### Possible applications with perpetual distributed video sensors?

![](_page_57_Figure_2.jpeg)

![](_page_58_Picture_0.jpeg)

#### • Any other solutions to reduce the power consumption?