National Chiao Tung University
Dept. of Computer & Information Science
Introduction to VLSI Design, Fall 2000

October 18, 2000
Handout #5
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Extended Homework #1: Name: ID:

Question 1.

(a) Design CMOS logic gates for the function $F = \overline{(A + BC)D}$. Please also indicate the connections for F and other required signals (e.g., GND, V_{DD}, etc).

(b) Draw a stick diagram representation for the circuit designed in (a).

Question 2. What are your suggestions/comments on this course? Please give at least one strength and one weakness. Any **specific** comments that may lead to the improvement of this course are very much welcome.