National Chiao Tung University
Dept. of Computer & Information Science
Introduction to VLSI Design, Fall 2000

November 3, 2000
Handout #8
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Sample Solutions to Extended Homework #1

1. (a) See Fig. 1

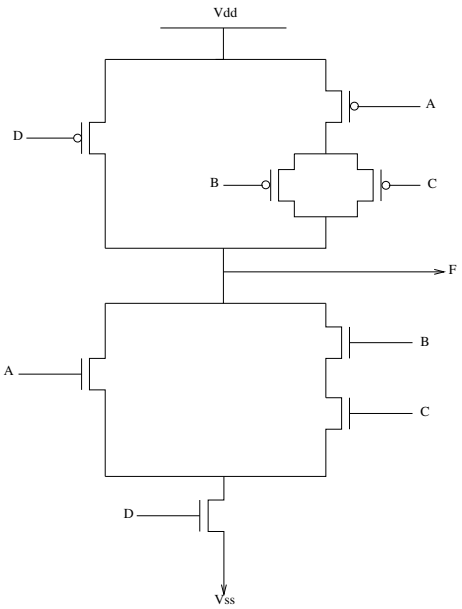

Figure 1: The CMOS logic gates for the function $F = \overline{(A + BC)D}$.

(b) See Fig. 2

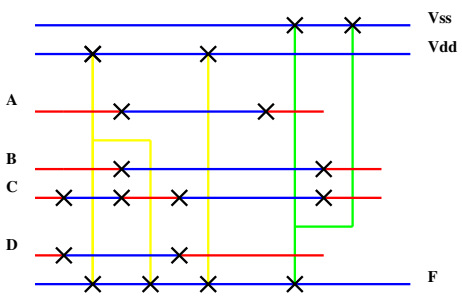

Figure 2: The stick diagram for the function $F = \overline{(A + BC)D}$.