# **INDUCTANCE MODELING FOR ON-CHIP INTERCONNECTS**

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### ABSTRACT

As the operation frequency reaches gigahertz in deep-submicron designs, the effects of inductance on noise and delay can no longer be neglected. Most of the previous works on inductance extraction are fieldsolvers, which are intrinsically more accurate but computationally expensive. Others focus on modeling the inductances of special routing topologies such as the bus structure. Therefore, it is not suitable to incorporate them on-line into a layout (placement and routing) tool for inductance (delay and noise) optimization. In this paper, we consider the overlapping of unequal wire lengths and dimensions to efficiently extract the loop inductance from the coplanar interconnect structure. The difference between our simulation results and the estimation values obtained by FastHenry [12] is within 10% for practical cases. In particular, our method is very fast. Based on our study, we also suggest several routing topologies for inductance minimization.

### **1. INTRODUCTION**

As technology advances into the very deep-submicron era, interconnection delay dominates overall circuit performance and noise becomes more serious than before. Therefore, accurately predicting the interconnection delay and noise becomes a major challenge in high performance designs. For deep-submicron, high-performance circuits, ignoring inductance effects may incur a large amount of error, since an RC model as compared to an RLC model may create errors of up to 30% in the total propagation delay of a repeater system [11], and in some worst cases the noise coupling due to C and L may reach around 55% of the supply voltage [9]. As technology improves and die size increases, short rise/fall times of signals and long wires make inductance effects much more significant than before [14]. Therefore, it is very importance to consider the self and coupling inductance.

Inductance extraction has been studied extensively in the literature. Grover [8] collected many formulas for extracting inductance. However, as mentioned in [10], there are limitations of applying the equations. For example, they do not consider internal inductance, and wire widths are not considered for mutual inductance. Greengard developed a *multipole method* to extract inductances [7]. Phillips, Kamon, and White [13] proposed an *FFT-based approach* for 3-D inductance extraction. Beattie and Pileggi proposed a *hierarchical refinement method* [3] that improves the running time for capturing the near field effects. Also, Beattie et al. proposed equipotential shells methodology [2] to efficiently extract the inductance of the bus structure. He et al. presented a table-based approach to identify the structure of a circuit for inductance extraction [10].

Most of the previous works are field-solvers that are intrinsically more accurate but are computationally expensive. Others focus on modeling the inductances of special routing topologies such as the bus structure. Therefore, it is not suitable to incorporate them on-line into a layout tool for inductance optimization. As [5] pointed out, no good approximation formula exists for coupling inductances of two parallel lines of unequal lengths and dimensions. We consider the overlapping of unequal wire lengths and dimensions (widths and heights) to extract the self and coupling inductance. We derive formulae and models to approximate both self and coupling inductance efficiently yet accurately for the coplanar routing structure. Simulation results show that the difference of the inductance values extracted from our models for practical cases is within 10% for the wires of unequal lengths and dimensions, compared with

FastHenry [12]. In particular, our method is extremely fast. For example, the running time for extracting the coupling inductance of two wires of 2000 um by using our analytical formulas is typically within 0.11 seconds using Mathematica on a 566 MHz Cerelon PC with 128 MB RAM while FastHenry requires 141 seconds on SUN Sparc Ultra 60 with dual CPUs and 2GB memory. Therefore, it is feasible to incorporate our model into a layout tool on-line.

Recently, researchers presented layout structures to reduce inductance such as staggered inverter [4] and twisted-bundle layout structure [15]. Based on our inductance modeling, we also suggest good routing topologies for inductance reduction. Simulation results justify our findings.

The rest of this paper is organized as follows. Section 2 describes the coplanar interconnect structure and some basic electromagnetic concepts. Section 3 derives formulae for self and coupling inductance. Section 4 proposes some routing topologies for inductance minimization. Section 5 gives the simulation results on the comparisons between our methods and FastHenry. Finally, Section 6 concludes our work.



Figure 1: Coplanar interconnect structure.

Figure 1 depicts the coplanar interconnect structure. Here, *s* stands for a signal wire while *P* and *G* stands for a power and a ground grid of identical width,  $w_g$ . Throughout this paper, both *P* and *G* are considered as ground wires. Assume that all wires are of the same height *h*, the width and length of each signal wire are  $w_s$  and  $l_w$  respectively, and the overlapping length of two signal wires is *l*. Throughout this paper, we assume *quasi-static conditions* [1].

Before deriving the inductance for a wire, we shall first determine the magnetic flux density of the wire carrying current *I* by using *Ampère's circuital law* [6]:

$$\vec{B} \cdot d\vec{l} = \mu_0 I \,, \tag{1}$$

where **B** is magnetic flux density, the path C for the line integral is the contour bounding the surface S, I is the total current through S, and  $\mu_0$  is permeability whose value is  $4\pi \times 10^{-7}$  (H/m).

Then we derive the self-inductance  $(L_i)$  and coupling inductance  $(L_{ij})$  as in Equations (2) and (3), respectively, by using *Faraday's law* [6]:

$$L_{i} = \frac{\Lambda_{i}}{I_{i}} = \frac{N_{i}\Phi_{i}}{I_{i}} = \frac{N_{i}}{I_{i}} \int_{S_{i}} \vec{B}_{i} \cdot d\vec{s}_{i}, \qquad (2)$$

$$L_{ij} = \frac{\Lambda_{ij}}{I_{i}} = \frac{N_{j}\Phi_{ij}}{I_{i}} = \frac{N_{j}}{I_{i}} \int_{S_{j}} \vec{B}_{i} \cdot d\vec{s}_{j}. \qquad (3)$$

In Equation (2),  $\Lambda_i$  is flux linkage,  $\Phi_i$  is flux,  $S_i$  represents the surface bounded by the loop C of wire *i*,  $N_i$  is the number of turns of loop C, and  $B_i$  is the magnetic flux density due to current  $I_i$  in wire *i*. In Equation (3),  $S_i$  is the surface bounded by the loop  $C_i$  of wire *j*,  $N_i$  is the number of turns of loop  $C_i$ , and  $B_i$  is the magnetic flux density due to current  $I_i$  in wire *i*.

Same as [15], we assume that all signals use the nearest P/G wires as their return paths, and other wires that are not involved in the computation for the inductance between wires *i* and *j* are floating.



3.1. Self-inductance



Figure 2: (a) Degeneration of metal wire from Figure 1. (b) Cross-section view of circular conductor.

Because the rectangular shape of wires, as illustrated in Figure 1, complicates the computation of magnetic flux density, we apply circular conductors to approximate the results, as illustrated in Figure 2(a). If the current  $I_i$  in wire  $s_i$  flows as the direction shown in Figure 2(a), the current loop *C* can be determined as the dashed line. The area of  $S_i$ , which is the gray region in Figure 2, is determined by the enclosed region of the loop *C*. To derive self-inductance from Equation (1), we must know  $N_i$ ,  $S_i$ , and  $B_i$ . Although  $N_i = 1$  and  $S_i$  are already known,  $B_i$  is still unknown. Hence, we shall derive the formula of  $B_i$  first. Here, the unit of *L*' is henry per meter (H/m) and the unit of *L* is henry (H).

Figure 2(b) depicts the cross-sectional view of a circular conductor whose radius is *b*. If we align the conductor's center along the *z*-axis in the cylindrical coordination system, current  $I_i$  flows in the *z* direction. Assume that current  $I_i$  is uniformly distributed in the conductor, and the conductor is sufficiently long  $(l_w >> r)$ , we can derive Equations (4) and (5) by Equation (1) as follows:

$$\vec{B}_{in} = \hat{a}_{\phi} \frac{\mu_0 r_1 I_i}{2\pi b^2} , \quad r_1 \le b$$

$$\vec{B}_{out} = \hat{a}_{\phi} \frac{\mu_0 I_i}{2\pi r_2} , \quad r_2 \ge b$$
(5)

where  $B_{in}$  is the magnetic flux density inside the conductor and  $B_{out}$  is the magnetic flux density outside the conductor.

We derive the formula of the internal inductance per unit length for a circular wire. Consider an annular ring in the inner conductor between radius r and r+dr ( $r \le b$ ). The current in a unit length of this annular ring is linked by the flux that can be obtained from Equation (4), we have

$$d\Phi'_{in} = \int_{r}^{b} B_{in} dr = \frac{\mu_0 I_i}{4\pi b^2} (b^2 - r^2) \cdot$$
(6)

The current in the annular ring, however, is only a fraction  $(2\pi r dr/\pi b^2 = 2r dr/b^2)$  of the total current  $I_i$ . Hence, we can obtain the unit-length internal inductance of the circular wire by using Equations (2) and (6):

$$L'_{in} = \frac{\Lambda'_{im}}{I_i} = \frac{\int_0^b d\Lambda'_{in}}{I_i} = \frac{\int_0^b 2r dr d\Phi'_{in}}{b^2 I_i} = \frac{\mu_0}{8\pi} \quad .$$
(7)

Then we derive the external self-inductance. In the *xz*-plane where the two wires lie, as in Figure 2, the contributing B vectors due to the equal and opposite currents in the two wires have only a *y*-component. Hence, from Equation (5), we have

$$B_G = \frac{\mu_0 I_i}{2\pi x} , \qquad (8)$$

$$I_i = \frac{\mu_0 I_i}{2\pi (d_g + r_g + r_s - x)} . \qquad (9)$$

Therefore, the unit-length external inductance of Figure 2 is

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$$L'_{ex} = \frac{\Phi'_{ex}}{I_i} = \frac{\int_{r_g}^{r_g + d_g} (B_G + B_{s_i}) dx}{I_i} = \frac{\mu_0}{2\pi} \ln\left(\frac{(r_g + d_g)(r_s + d_g)}{r_s r_g}\right).$$
(10)

Finally, we can derive the total self-inductance  $(L_i)$  of Figure 2 from Equations (7), (10), and the signal wire length  $l_w$  as follows:

$$L_{i} = l_{w}(2L_{in}' + L_{ex}') = l_{w}\left(\frac{\mu_{0}}{4\pi} + \frac{\mu_{0}}{2\pi}\ln\left(\frac{(r_{g} + d_{g})(r_{s} + d_{g})}{r_{s}r_{g}}\right)\right) \quad (11)$$

If another ground wire exists on the other side of the signal wire separated by the distance  $d_{g2}$  ( $d_{g2} \ge d_g$ ), the current flowing in the nearest *G* wire will decrease. We modify Equation (11) to Equation (12) with the branch current of  $0.5I_i$  in the *G* wire, which is given by the *Kirchhoff's Current law*.

$$L_{i} = \frac{l_{w}\mu_{0}}{2\pi} \left( 0.375 + \ln\left(\frac{(r_{g} + d_{g})^{0.5}(r_{s} + d_{g})}{r_{s}r_{g}^{0.5}}\right) \right)$$
(12)

If there are *n* ground wires, we can simplify these cases to the two ground wires system. We named the above formula *AMAS* (<u>A</u>nalytical <u>M</u>odel <u>Approximation of <u>S</u>elf-inductance).</u>

# **3.2.** Coupling Inductance



Figure 3: Top view of the coplanar structure.

As in the previous subsection, we consider single ground wire system first. The top view of the coplanar structure is shown in Figure 3. Now, we consider the *xz*-plane where the three wires lie. The **B** vectors due to the currents in G and  $s_i$  wires have only a y-component. From Equation (5), we have

$$B_G = \frac{\mu_0 I_i}{2\pi x} \tag{13}$$

$$B_{s_i} = \frac{\mu_0 I_i}{2\pi (0.5w_g + d_g + d_s + 1.5w_s - x)}$$
 (14)

The coupling inductance  $L_{ij}$  is proportional to the overlapping area of  $S_i$  and  $S_j$ . The overlapping area  $S_{lap}$  is shown as the gray region in Figure 3. We can derive the external inductance per unit length from Equation (3) as follows:

$$L_{ex}' = \frac{\Lambda_{ex}'}{I_i} = \frac{\Phi_{ex}'}{I_i} = \frac{1}{I_i} \int_{S_{log}} Bds_{lap} = \int_{0.5w_g}^{0.5w_g + d_g + w_s} (B_G + B_{s_i}) dx$$
$$= \frac{\mu_0}{2\pi} \ln \left( \frac{(0.5w_g + d_g + w_s)(d_g + d_s + 1.5w_s)}{0.5w_g (d_s + 0.5w_s)} \right).$$
(15)

The internal inductance due to the G wire is the same as that of Equation (7). Hence, the total coupling inductance shown in Figure 3 is  $L_{ii} = L_{ii} = l(L'_{in} + L'_{ex})$ 

$$=\frac{l\mu_0}{2\pi}\left(\frac{1}{4}+\ln\left(\frac{(0.5w_g+d_g+w_s)(d_g+d_s+1.5w_s)}{0.5w_g(d_s+0.5w_s)}\right)\right).$$
 (16)

If another ground wire exists on the other side of the signal wire at distance  $d_{g2}$  ( $d_{g2} \ge d_g + d_s + w_s$ ), the current flowing in the nearest *G* wire will decrease. Again, we can modify Equation (16) to (17) with the branch current of  $0.5I_i$  in the *G* wire as follows:

$$L_{ij} = L_{ji} = \frac{l\mu_0}{2\pi} \left( \frac{1}{8} + \ln \left( \frac{(0.5w_g + d_g + w_s)^{0.5} (d_g + d_s + 1.5w_s)}{(0.5w_g)^{0.5} (d_s + 0.5w_s)} \right) \right) (17)$$

If there are *n* ground wires, we can simplify these cases to the two ground wires system. We name the above formula *AMAC* (<u>A</u>nalytical <u>M</u>odel <u>Approximation of C</u>oupling Inductance).

### 3.3. Modeling Short Wires and Small Coupling

Previous subsections are all based on the assumption that wires are sufficiently long, namely,  $l_w >> d_g$ . When wire lengths are comparable to wire spaces, however, Equation (5) may no longer be accurate. Further, coupling inductance still exists even if two signal wires are not overlapped. Therefore, the equations derived in Sections 3.1 and 3.2 may cause significant errors in some corner cases. To cope with these problems, we should use more sophisticated formulas to approximate the inductance for the cases when (a) the wire length is comparable to its distance to the nearest ground wire, and (b) the overlapping length is much smaller than the signal wire length.



Figure 4: (a) Top view of a carrying current wire. (b) Top view of the two-ground wires system.

Equation (18), the *Biot-Savart law*, gives the magnetic flux density for condition (a) or (b):

$$\vec{B} = \oint_{C'} d\vec{B} = \frac{\mu_0 I}{4\pi} \oint_{C'} \frac{d\vec{l}' \times \vec{R}}{R^3}$$
(18)

where C' is the closed path of current I, and  $\vec{R}$  is the vector directed from the source to the field point. If we have a line segment carrying current I along the z-axis in the cylinder coordinate shown in Figure 4(a). The resulting **B** vector is

$$\vec{B} = \hat{a}_{\phi} \frac{\mu_0 I}{4\pi} \int_0^{I_w} \frac{r dz'}{\left(r^2 + (z - z')^2\right)^{3/2}}.$$
 (19)

Next, we consider the coplanar structure of Figure 4(b). From Equation (19), we know the contributing **B** vectors due to  $s_i$  and G wires of Figure 4(b). The flux linkages due to  $s_i$  and G wires are

$$\Lambda_{G} = \Phi_{G} = \int_{S_{j}} B_{G} ds_{j} = \int_{0.5w_{g}}^{0.5w_{g}+d_{g}+w_{s}} \int_{l-l_{w}}^{l} B_{G} dz dx, \qquad (20)$$

$$\Lambda_{s_i} = \Phi_{s_i} = \int_{S_j} B_{s_i} ds_j = \int_{0.5w_g}^{0.5w_g + d_g + w_s} \int_{l-l_w}^{l} B_{s_i} dz dx \cdot$$
(21)

Therefore, the total external inductance is

$$L_{ex} = \Lambda_{ex} / I_i = (\Lambda_G + \Lambda_{s_i}) / I_i$$
 (22)

Hence, the total coupling inductance is

$$L_{ij} = L_{in} + L_{ex} = 0.5l \frac{\mu_0}{8\pi} + L_{ex}$$
 (23)

We name the above formula IAMAC (Improved Analytical Model Approximation of Coupling Inductance).

Next, we derive the formula of the self-inductance for the situation when a signal wire length is comparable to its distance to the nearest ground wire. Considering the two ground wires system of Figure 4(b), the internal inductance is

$$L_{in} = (1+0.5) \times l_{w} \times \frac{\mu_{0}}{8\pi}$$
 (24)

By Equation (2), we have

$$\Lambda_{G} = \Phi_{G} = \int_{S_{j}} B_{G} ds_{j} = \int_{0.5w_{g}}^{0.5w_{g} + d_{g}} \int_{l-l_{w}}^{l} B_{G} dz dx , \qquad (25)$$
  
$$\Lambda_{s_{l}} = \Phi_{s_{l}} = \int_{S_{j}} B_{s_{l}} ds_{j} = \int_{0.5w_{s}}^{0.5w_{s} + d_{g}} \int_{l-l_{w}}^{l} B_{s_{l}} dz dx . \qquad (26)$$

 $B_G$  and  $B_{si}$  are known from Equation (19), and the total self-inductance is

$$L_{i} = L_{in} + L_{ex} = 1.5 l_{w} \frac{\mu_{0}}{8\pi} + L_{ex}$$
(27)

We name the above formula *IAMAS* (*Improved* <u>A</u>nalytical <u>M</u>odel <u>Approximation of</u> <u>Self-inductance</u>).

# 4. LAYOUT IMPLICATIONS

In this section, we suggest some better routing topologies to reduce coupling inductance. As an example, if there are three terminals—one source and two sinks as shown in Figure 5—to be routed from the source A to two sinks B and C, which one in Figure 6 is the best routing topology to reduce the coupling inductance? We used FastHenry to extract the coupling inductance of each routing topology and obtained the results listed in Table 1.







Figure 6:Potential routing topologies of Figure 5 (source: A).

From Equation (3),  $S_j$  and  $B_i$  are the two major factors in determining the value of coupling inductance. As shown in Table 1, *Topology 6* results in the smallest coupling inductance because it has the smallest  $S_2$  among the six routing topologies. Therefore, Topology 6 has the smallest  $L_{12}$ because the  $B_I$  from  $s_I$  is identical to all topologies. Considering the optimization of coupling inductance in this example, Topology 6 is the best choice even though it has the largest wire length

| best enoise even mough it has the largest whe length. |                             |                             |  |  |  |  |  |
|---|-----------------------------|-----------------------------|--|--|--|--|--|
| Topology  | Coupling inductance         | Coupling inductance ranking |  |  |  |  |  |
| Topology  | extracted by FastHenry      | (from low to high)          |  |  |  |  |  |
| 1   | 2.82042×10 <sup>-10</sup> H | 2                           |  |  |  |  |  |
| 2   | 2.82579×10 <sup>-10</sup> H | 3                           |  |  |  |  |  |
| 3   | 3.64932×10 <sup>-10</sup> H | 4                           |  |  |  |  |  |
| 4   | 5.21743×10 <sup>-10</sup> H | 5                           |  |  |  |  |  |
| 5   | 5.64098×10 <sup>-10</sup> H | 6                           |  |  |  |  |  |
| 6   | 0.48866×10 <sup>-10</sup> H | 1                           |  |  |  |  |  |
|   |                             |                             |  |  |  |  |  |

Table 1: Coupling inductance of the routing topologies in Figure 6.

We also consider the effect of the assignment for the source pin on inductance: Will the coupling inductance be different if we change the source? For Topology 6, the results are shown in Table 2 for assigning A, B, or C as the source of the net

| Source in  | Coupling inductance    | Coupling inductance ranking |  |  |  |  |  |  |  |
|------------|------------------------|-----------------------------|--|--|--|--|--|--|--|
| Topology 6 | extracted by FastHenry | (from low to high)          |  |  |  |  |  |  |  |

| A | 0.488662×10 <sup>-10</sup> H | 2 |
|---|------------------------------|---|
| В | 0.480616×10 <sup>-10</sup> H | 3 |
| С | 0.008044×10 <sup>-10</sup> H | 1 |

 
 Table 2: Results of coupling inductances for different source assignments in Topology 6.

From Table 2, making C the source pin results in the smallest coupling inductance. This phenomenon is due to the fact when current flows from C to A and B, the currents in line segments  $\overline{GJ}$  and  $\overline{GD}$  of Topology 6 result in the magnetic fluxes of opposite directions. Therefore, source-pin assignment is also an important issue for coupling-inductance optimization, for which we suggest that the source pin be assigned to the pin in the middle.





Figure 7: Cross-section view of the coplanar structure.

We conducted the following experiments: (1) the accuracy of AMAS and IAMAS for self-inductance extraction, and (2) the accuracy of AMAC and IAMAC for coupling-inductance extraction.

In the first experiment, we tested the accuracy of AMAS and IAMAS for self-inductance extraction based on the structure of Figure 7 with various l<sub>w</sub>'s (signal wire lengths), d<sub>g</sub>, w<sub>s</sub>, w<sub>g</sub>. Also, we set  $d_{g2} = d_g = 18.4$  $\mu$ m and h = 2  $\mu$ m. The length of the G wire was 4500 $\mu$ m. The experimental results are given in Table 3, for which FastHenry was run on a SUN Ultra 60 model 1450 with dual CPUs and 2GB RAM and AMAS and IAMAS on a 566 MHz Cerelon PC with 128 MB RAM by using Mathematica Version 3.0. Identical values of parameters and conditions were applied to the second experiments on the coupling inductance whose results are shown in Table 4. Compared with FastHenry, the average errors of our modeling are within 10%, except for the two corner cases: (1) the wire length is comparable to its distance to the nearest ground wire, and (2) the overlapping length is much smaller than the signal wire length. This phenomenon justifies our observation in Section 3.3. For these corner cases, our improved formulae can achieve significantly more accurate estimations than the original ones, as shown in Tables 3 and 4. The results show the accuracy of our work. In particular, our method is extremely fast. For example, the running time for extracting the coupling inductance of two wires of 2000 um by using our analytical formulas is typically within 0.11 seconds using Mathematica on a 566 MHz Cerelon PC with 128 MB RAM while FastHenry requires 141 seconds on SUN

Spare Ultra 60 with dual CPUs and 2GB memory. Therefore, it is feasible to incorporate our model into a layout tool on-line.

## 6. CONCULSION

We have derived analytical formulae to efficiently approximate the self and coupling inductance for wires of unequal lengths and dimensions. Experimental results have shown the accuracy of our formulae. In particular, our modeling is extremely efficient, and thus can be incorporated into a layout tool for inductance optimization. Based on our studies, we also have suggested some good routing topologies of signal wires to reduce the coupling inductance. Our findings should be very useful for layout designs with inductance considerations.

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| <br>                                   |                              |            |                             |            |           |                             |            |           |
|--|------------------------------|------------|-----------------------------|------------|-----------|-----------------------------|------------|-----------|
| $(l_{w}, d_{g}, w_{s}, w_{g}) (\mu m)$ | FastHenry                    | Time (sec) | AMAS                        | Time (sec) | Error (%) | IAMAS                       | Time (sec) | Error (%) |
| (2000, 12, 0.8, 2)                     | 18.70280×10 <sup>-10</sup> H | 117.09     | 20.3658×10 <sup>-10</sup> H | 0.11       | 8.892     | 20.3299×10 <sup>-10</sup> H | 8.02       | 8.700     |
| (1000, 12, 0.8, 2)                     | 9.99291×10 <sup>-10</sup> H  | 103.41     | 10.1829×10 <sup>-10</sup> H | 0.11       | 1.901     | 10.1470×10 <sup>-10</sup> H | 5.82       | 1.542     |
| (500, 12, 0.8, 2)                      | 5.14302×10 <sup>-10</sup> H  | 106.79     | 5.0915×10 <sup>-10</sup> H  | 0.11       | -1.003    | 5.0557×10 <sup>-10</sup> H  | 5.72       | -1.698    |
| (10, 12, 0.8, 2)                       | 0.07443×10 <sup>-10</sup> H  | 98.60      | 0.1018×10 <sup>-10</sup> H  | 0.11       | 36.806    | 0.0761×10 <sup>-10</sup> H  | 6.15       | 2.222     |
| (1000, 12, 0.8, 4)                     | 9.26282×10 <sup>-10</sup> H  | 111.57     | 9.3216×10 <sup>-10</sup> H  | 0.11       | 0.635     | 9.5280×10 <sup>-10</sup> H  | 5.66       | 2.863     |
| (1000, 12, 1.6, 2)                     | 9.60097×10 <sup>-10</sup> H  | 110.11     | 8.8601×10 <sup>-10</sup> H  | 0.11       | -7.716    | 8.8243×10 <sup>-10</sup> H  | 5.71       | -8.090    |
| (1000, 6, 0.8, 2)                      | 7.73106×10 <sup>-10</sup> H  | 100.60     | 8.2411×10 <sup>-10</sup> H  | 0.05       | 6.597     | 8.2231×10 <sup>-10</sup> H  | 5.66       | 6.365     |
| Average (absolute value)               |                              | 106.88     |                             | 0.10       | 9 079     |                             | 6.11       | 4 497     |

Table 3: Experimental results of our modelings, AMAS and IAMAS, and FastHenry for self-inductances. The lengths of ground wires are 4,500 µm.

| Signal wire<br>length (µm) | Overlapping<br>length (µm) | FastHenry                   | Time (sec) | AMAC                        | Time (sec) | Error (%) | IAMAC                       | Time (sec) | Error (%) |
|----------------------------|----------------------------|-----------------------------|------------|-----------------------------|------------|-----------|-----------------------------|------------|-----------|
| 2000                       | 2000                       | 17.1868×10 <sup>-10</sup> H | 141.67     | 17.3525×10 <sup>-10</sup> H | 0.11       | 0.964     | 17.2998×10 <sup>-10</sup> H | 6.10       | 0.657     |
| 2000                       | 1500                       | 12.2176×10 <sup>-10</sup> H | 141.98     | 13.0143×10 <sup>-10</sup> H | 0.11       | 6.521     | 13.0140×10 <sup>-10</sup> H | 15.82      | 6.518     |
| 1000                       | 1000                       | 9.2767×10 <sup>-10</sup> H  | 114.34     | 8.6762×10 <sup>-10</sup> H  | 0.06       | -6.473    | 8.6237×10 <sup>-10</sup> H  | 5.66       | -7.039    |
| 1000                       | 500                        | 4.3076×10 <sup>-10</sup> H  | 116.70     | 4.3381×10 <sup>-10</sup> H  | 0.05       | 0.709     | 4.3379×10 <sup>-10</sup> H  | 5.22       | 0.705     |
| 500                        | 400                        | 3.7163×10 <sup>-10</sup> H  | 110.28     | 3.4705×10 <sup>-10</sup> H  | 0.05       | -6.614    | 3.4684×10 <sup>-10</sup> H  | 17.09      | -6.669    |
| 500                        | 200                        | 1.7031×10 <sup>-10</sup> H  | 110.00     | 1.7353×10 <sup>-10</sup> H  | 0.11       | 1.887     | 1.7352×10 <sup>-10</sup> H  | 9.12       | 1.884     |
| 100                        | 50                         | 0.4887×10 <sup>-10</sup> H  | 105.41     | 0.4338×10 <sup>-10</sup> H  | 0.11       | -11.237   | 0.4321×10 <sup>-10</sup> H  | 4.94       | -11.586   |
| 100                        | 25                         | 0.2422×10 <sup>-10</sup> H  | 106.55     | 0.2169×10 <sup>-10</sup> H  | 0.06       | -10.435   | 0.2191×10 <sup>-10</sup> H  | 11.70      | -9.523    |
| 100                        | 5                          | 0.0547×10 <sup>-10</sup> H  | 109.70     | 0.0434×10 <sup>-10</sup> H  | 0.05       | -20.706   | 0.0561×10 <sup>-10</sup> H  | 12.03      | 2.553     |
| A                          | Average (absolu            | te value)                   | 117 40     |                             | 0.08       | 7.283     |                             | 9.74       | 5.683     |

Table 4: Experimental results of our modelings, AMAC and IAMAC, and FastHenry for coupling inductances. Here,  $w_s = d_s = 0.8 \mu m$  for both signal wires, both ground wire length = 4,500 \mu m,  $w_g = 2\mu m$ ,  $d_g = 12 \mu m$ .